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(54) **Adaptive receiver for CDMA communication system**

(57) An adaptive signal receiver comprising at least one blind detection unit arranged to be robust to asynchronous multiple access interference (MAI). The useful signal is detected using a user signature sequence com-

prised of a fixed term and a complex adaptive part having a length that extends over a number of samples within a defined observation window. Provision is made for updating automatically and periodically the complex adaptive part of the signature sequence.

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Description**Field of the invention**

5 [0001] The present invention relates to the field of Code Division Multiple Access (CDMA) communications with direct sequence/spread spectrum (DS/SS) modulated signals. It is concerned in particular with a novel adaptive signal receiver for CDMA communication systems.

Background of the invention

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[0002] A CDMA communication system transmits information from a number of users over a common channel using different code sequences referred to as signatures. In these systems, the transmitters send the information independently from each other. Therefore, signals from different users arrive asynchronously at the receiver. Because the cross correlations between the signatures for signals from different users are non zero, the received signal from a user is sensitive to nearby interfering signals from other users.

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[0003] This sensitivity to interference from other users constrains the number of active users operating at a specified system bit error rate (BER), and therefore CDMA systems are said to bear a modest overall capacity in terms of number of users/bandwidth.

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[0004] Some attention has already been focused on the Multiple Access Interference (MAI) and in the last decade there has been an explosion of interest on multiuser interference rejection and multiuser detection (MUD). The purpose of MUD is to achieve the joint demodulation of all user data streams. One of the major outputs of the extensive research activity, inspired by the discovery of MUD, was the recognition that the sensitivity to MAI of CDMA is not inherent to the access method itself, rather it is essentially related to the incorrect utilization, in a multiple-access environment, of the single-user correlation receiver that presently represents the optimum scheme for processing a single DS/SS signal in an additive white Gaussian noise (AWGN) channel (see "Optimum Multiuser Asymptotic Efficiency" by S. Verdú, IEEE Transactions on Communications, September 1986, pp. 890-897).

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[0005] The implementation of a multiuser receiver is not straightforward : firstly due to the high computational complexity for the optimum scheme and secondly because even sub-optimum lower-complexity schemes call for accurate estimation of the code timing, the carrier phase and/or the signal power of all the active network users. The latest step towards a practical implementation of a MAI-resilient detector is represented by the class of single-user adaptive receiver an example of which is disclosed for instance in "MMSE Interference Suppression for Direct-Sequence Spread-Spectrum CDMA", by Upamanyu Madhow et Michael L. Honig, IEEE Transactions on Communications, Vol. 42, No. 12, December 1994, pp. 3178-3188. This type of receiver is capable of eliminating a substantial portion of the multiuser interference, but it needs to be adjusted by a proper training sequence that must be known a priori, before data transmission can occur.

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[0006] A robust and simple receiver of this kind that can operate without need for a training sequence is disclosed in "Blind Adaptive Multiuser Detection", by Michael Honig, Upamanyu Madhow, Sergio Verdú, IEEE Transactions on Information Theory, Vol. 41, No. 4, July 1995, pp. 944-960. The simulation results presented therein are relative to the case of BPSK DS/SS synchronous signals with rectangular chips, ideal coherent detection and a low AWGN level. The ideal coherent detection is in fact a strong assumption that largely reduces the applicability of blind adaptive interference-mitigating detection (BAID) scheme to practical systems. The use of the BAID detector requires the prior knowledge of the useful channel carrier frequency and phase that is generally unknown to the receiver and that can not be accurately estimated prior to DS/SS signal detection.

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[0007] In particular, the authors of said publication envisage application of BAID to the initial receiver acquisition phase only, just to bring the signal-to-noise ratio after detection in the vicinity of 0 dB.

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[0008] In such conditions, a decision-aided detector can directly take over without the need of a special training sequence. Such decision-aided detector is disclosed in "Adaptive Receiver Structures for Asynchronous CDMA Systems", by Predrag B. Rapajic and Branka S. Vucetic, IEEE Journal on Selected Areas in Communications, Vol. 12, No. 4, May 1994, pp. 685-697. Although optimal in performance, a data-aided detector requires the prior knowledge of the transmitted symbols. Assuming a slowly variant channel as compared to the symbol duration, it is possible to insert a preamble of known data in the data stream at regular intervals to help the detector convergence.

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[0009] In this case, however, there is a penalty due to the increased data rate and the need for fast converging algorithms like Recursive Least Square that drastically increases the algorithm complexity and presents numerical stability problems. While the decision-aided approach does not require the prior knowledge of a training sequence, it has however a major drawback in its poor performance under low signal-to-noise ratio conditions whereby symbol estimates become unreliable and detector performance rapidly degrades. This behavior is particularly harmful when the signal amplitude significantly fades, as is the case in many practical (mobile) systems or when path diversity is exploited.

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[0010] In this case, equalization has to be performed on each Rake receiver finger at lower input signal-to-noise ratio. Furthermore, the adaptive detection approach in this known detector is not insensitive to the unknown phase of the useful signal and the useful channel carrier frequency error.

[0011] It is known that the data-aided MMSE algorithm provides intrinsic phase estimation on top of interference mitigation. This is an apparent advantage as it relates the algorithm convergence speed for interference mitigation to the carrier phase tracking. Over fading channels the algorithm performs poorly unless the useful signal phase is pre-corrected before entering the adaptive MMSE detector (see A.N. Barbosa, S.L. Miller "Adaptive Detection of DS/CDMA Signals in Fading Channels" IEEE Trans. on Comm., Vol. 46, No. 1, January 1998 whereby a solution based on a complicated phase estimates to remove phase errors at the detector input is proposed). The proposed solution being phase transparent does not suffer from this important problem and allows to effectively separate the useful signal phase estimation from CDMA interference mitigation.

Summary of the Invention

[0012] The present invention deals with an extension of the blind adaptive interference mitigating detection (BAID) scheme to the general case of asynchronous CDMA signals with arbitrary interferer timing and frequency offset in order to enhance the performance of a DS-CDMA receiver with affordable complexity increase.

[0013] It is an object of this invention to provide a blind adaptive receiver for CDMA signals which minimises the detrimental effect of multiple access interference (MAI) on the bit-error rate (BER) performance and which does not require using a known training sequence.

[0014] A further object of this invention is to provide a low-complexity blind adaptive receiver for CDMA signals that is robust to asynchronous MAI.

[0015] Yet another object of this invention is to provide a blind adaptive receiver scheme that is robust to the interferers frequency error and is invariant to a possible carrier phase offset.

[0016] These and other objects of the invention are attained by an adaptive receiver for CDMA signals which comprises means arranged to perform the symbol detection in the incoming data stream using a user signature sequence comprised of a first fixed term and a second complex adaptive part having a predetermined length that extends over a number of samples. Means are provided to update automatically and at successive regular intervals encompassing the duration of one symbol or a plurality of symbols.

[0017] The second complex adaptive part is determined starting from the error signal of the carrier frequency measured on the signal obtained after detection.

[0018] The blind adaptive detector incorporated in the CDMA receiver of the invention can be implemented in modular form well suited for a compact ASIC digital implementation on a user terminal. The embodiment includes unobvious techniques to avoid the impact of a definite number of bits being used by DSP/ASIC to represent the signals.

[0019] The solutions proposed herein are particularly suited for application to satellite communication systems, but they can be adapted for use in terrestrial communication systems as well. Also, the invention can be adapted for use in communication systems having a variable transmission rate as proposed for the third-generation standards for wireless CDMA communication.

[0020] The main advantages of the invention can be summarized as follows :

1. There is no need for training sequences to help algorithm convergence nor knowledge of interferer parameters. The only parameters required are the useful channel signature sequence and chip timing information as for a conventional detector.

2. Robustness to asynchronous CDMA interference even for large interferer power unbalance and carrier frequency offsets.

3. The detector is insensitive to the unknown phase of the useful signal and can adopt conventional BPSK symbol rate phase estimators.

4. Increased robustness to the residual baseband useful channel carrier frequency error as compared to conventional data-aided Minimum Mean Square Equalizer (DA-MMSE).

5. System performance is very close to the optimum DA-MMSE linear detector in the SNR region of practical interest for a coded system.

6. The adaptive detector can operate at very low SNR typical of a coded system and supports more spectral efficient Dual-BPSK Spread Spectrum signal formats (D-BPSK/SS).

7. It is well suited for a compact ASIC digital implementation on a user terminal.

[0021] The invention can find practical applications in the following domains :

- 5 . Fixed and mobile satellite communication networks.
- . CDMA-based positioning systems.
- . Terrestrial CDMA networks.

Brief Description of the Drawings

[0022]

Fig. 1 is a block diagram of a prior art blind adaptive receiver;
 Fig. 2 is a block diagram of an adaptive receiver in accordance with the invention;
 15 Fig. 3 is a functional block diagram of the detection unit shown in the receiver of Fig. 2;
 Fig. 4 illustrates a variation of the adaptive receiver according to the invention;
 Figs. 5a and 5b are functional block diagrams of two implementations of the detection unit incorporated in the receiver of Fig. 4;
 Fig. 6 is a functional block diagram of a variation of the adaptive receiver of Fig. 4;
 20 Figs. 7a-7c are functional block diagrams of two implementations of the detection unit incorporated in the receiver of Fig. 6;
 Fig. 8 shows an optimized architecture for a variation of the detection unit according to the invention.
 Fig. 9 is a diagram showing the BER performance of a system incorporating the invention.
 Fig. 10 is a functional block diagram of a further variation of the adaptive receiver according to the invention.
 25 Fig. 11 illustrates the adaptation convergence of the adaptive receiver of the invention;
 Fig. 12 shows the variation of BER vs the adaptation factor Gamma for various detector types;
 Fig. 13 shows the variation of BER vs $\Delta f_k T_s$ for various detector types;
 Fig. 14 shows the variation of VER vs the number of interferers for various detector types;
 Fig. 15 shows the PDF vs factor ρ for various detector types;
 30 Fig. 16 shows the variation of BER vs the ratio E_s/N_0 for various detector types;
 Fig. 17 illustrates the simulation results showing BER vs the ratio E_s/N_0 for various detector types;
 Fig. 18 is a functional block diagram of another variation of the adaptive receiver of the invention;
 Fig. 19 is a block diagram of a multi-rate detector of the invention for the highest rate in a network;

Description of Exemplary Embodiments of the Invention

The CDMA Signal Format

40 [0023] The signal format introduced here is quite general and corresponds to DS/Spread Spectrum (DS/SS) with two-dimensional modulation. In the most general case the incoming binary data stream at rate R_b for the k-th user is split between the two Phase-Quadrature (P-Q) rails by means of a serial-to-parallel converter. The resulting symbols $a_{k,p}(u)$, $a_{k,q}(u) \in \{-1,1\}$ are independently spread by the P-Q signature sequences $c_{k,p}(l)$, $c_{k,q}(l)$ and filtered prior P-Q carrier modulation. The resulting complex k-th signal is given by

$$e_k(t) = \sqrt{D P_k} \sum_{u=-\infty}^{\infty} [a_{k,p}(u) s_{k,p}(t-uT_s-\tau_k) + j a_{k,q}(u) s_{k,q}(t-uT_s-\tau_k)] \cdot \exp[j(2\pi\Delta f_k t + \phi_k)]$$

$$s_{k,h}(t) = \sum_{l=1}^L c_{k,h}(l) g_T(t-lT_c), \quad h = p, q$$

where D is an amplitude factor related to the signal modulation dimensionality (see Table 1), P_k is the k-th signal power, L is the period for both spreading sequences, T_c is the chip time, $T_s = 1/(2R_b) = L T_c$ is the symbol time, Δf_k is the k-th

carrier frequency offset with respect to the nominal frequency f_0 , ϕ_k is the k-th user carrier phase, $g_T(t)$ is the impulse response of the chip shaping filter, and τ_k represents the k-th user signal delay. Without loss of generality we also assume $0 \leq \tau_k < T_s$.

[0024] The signature sequences $c_{k,h}(l)$ may be "compound" sequences such as Walsh Hadamard (WH) functions overlaid by extended pseudo-noise (PN) sequences having the same period and start epoch [1]. Compound sequences are necessary in multi-beam, multi-satellite systems, or cellular terrestrial systems wherein the overlay PN is beam/sector unique, and different WH signature sequences are assigned to each different user within the same beam/sector. Notice that we also assumed short codes, i.e., $T_s = L T_c$ in order for the EC-BAID to be applicable. In this case, the code length L is also coincident with the spreading factor T_s/T_c .

[0025] Equation (1) represents a variety of modulation and spreading formats as summarized in Table 1. For conventional detectors with symbol length spreading sequences d-BPSK and QPSK-RS doubles the available codebook size for orthogonal sequences with no bandwidth penalty. In case of carrier phase errors, d-BPSK provides increased robustness with respect to QPSK-RS [1].

[0026] The (baseband equivalent of the) received signal $r(t)$ is a multiplex of K different signals in the form (1), plus (the baseband equivalent of) the AWGN complex process $v(t)$ with two-sided power spectral density N_0 . Thus

$$r(t) = \sum_{k=1}^K e_k(t) + v(t)$$

Assuming now, for notation simplicity, that channel $k=1$ is the wanted channel we can re-write eqn. (2) as

$$r(t) = e_1(t) + J(t) + v(t)$$

Table 1 :

Signal formats					
Scheme	Modulation	Spreading	D	T_s/T_b	Properties
BPSK-RS	BPSK	real (RS)	2	1	$a_{k,q} = c_{k,q} = 0$
BPSK-CS	BPSK	complex	1	1	$a_{k,p} = a_{k,q}$
d-BPSK	2xBPSK	(cs)	1	2	$c_{k,p} \neq c_{k,q}$
QPSK-RS	QPSK	2xreal real (RS)	1	2	$c_{k,p} = c_{k,q}$

with $J(t) \triangleq \sum_{k=2}^K e_k(t)$ representing the MAI term. The sampled chip matched filter (CMF) output can be expressed as :

$$y(\tau) = r(t) \otimes g_R(t) \big|_{t=\tau T_c}$$

being $g_R(t)$ the CMF impulse response.

[0027] Referring now to Fig. 1, there is shown a prior art blind adaptive receiver as disclosed in the publication "Blind Adaptive Multiuser Detection" mentioned herein before. After being translate into the baseband, the signal $y(m)$ is received in a baseband filter 11, e.g. a Nyquist square-root raised cosine chip matched filter, the function of which is to limit the noise bandwidth without affecting the useful signal. The signal is then sampled at intervals T_c in sampler 12. Each sampling interval T_c is a sub-multiple of the symbol duration T_s . The complex samples are thereafter applied to a detector 13 for being processed to deliver the demodulated symbols at the symbol rate. To this end, the detector 13 receives the user code sequence as generated by a replica code generator 14. A replica code clock acquisition unit 15 is provided as it is in each conventional spread-spectrum demodulator. A frequency and phase estimator 16 serves to eliminate the frequency offset and carrier phase errors.

[0028] As mentioned above herein, the receiver according to the invention is designed and arranged in such a way as to cancel the multiuser interferences in a communication channel without need for a known training sequence prior to data transmission, nor any other information related to the interfering signals. For this purpose, the receiver performs a detection processing and incorporates means to detect the useful channel bit stream so as to minimize the mean

square error (MSE) between the actual detector output and the correct output we would get in the absence of noise and MAI.

[0029] Broadly stated, the adaptive detection processing is an extension of the one disclosed in the above-mentioned publication "Blind Adaptive Multiuser Detection", the content of which is incorporated herein by way of reference. The detection processing of the invention differs from the Michael Honig et al approach in that detection is made by performing a correlation on a number L of samples Y using a modified user signature sequence thereby to minimize the effect of interfering signals while despreading the signal without loss of performance.

[0030] In accordance with the invention, the user signature sequence is composed of two components : a fixed term c_1 (the anchor) and a complex adaptive part x_1 (auxiliary code) that is updated at least symbol per symbol using a recursive algorithm.

[0031] For the most simple C-VAID embodiment, this can be expressed as :

$$h_1(r) = c_1 + x_1(r)$$

$$c_1 = [c_1(1), \dots, c_1(L)]^T$$

where $h_1(r)$ is the signature sequence.

[0032] The auxiliary code has a length that encompasses the duration of at least two interfering symbols and it is automatically computed for each symbol or after several symbols using an adaptation rule that can be expressed by the following relations :

$$b'_1(r) = \frac{1}{L} C_1 - Y(r)$$

$$x_1(r) = x_1(r-1) - \gamma b_1(r) [Y(r) - \frac{Y(r)^T \cdot c_1}{L} \cdot c_1]$$

where

$b_1(r)$ is the detector output symbol,

γ is the update step to be set as a compromise between acquisition speed and steady-state performance.

[0033] Contrary to the prior art, the adaptation coefficients of the response vector $h_1(r)$ are complex valued as well as the detector output symbol $b_1(r)$.

[0034] This novel approach in accordance with this invention provides a number of advantages. First, the detector is rotationally phase invariant. This allows to exploit two (four in case of Dual BPSK format) projections of the received signal on the two (four) dimensions of the signal space, which are in general required to perform optimum coherent signal demodulation in the presence of carrier phase errors.

[0035] Second, the detector is resistant to non co-frequency MAI and this has big relevance to practical systems where large frequency offsets among different carriers occur with respect to the digital signaling rate.

[0036] A third advantage resides in that the complex blind adaptive receiver of the invention can be used with D-BPSK DS/SS signals, which minimizes the standard mean square error. This feature is very important for systems using short spreading sequences to exploit cyclo-stationary properties of the code. This is disclosed in "Two Different Philosophies in CDMA-A Comparison", by S. Ventú and A.J. Viterbi, IEEE Vehicular Technology Conference, Atlanta, Georgia, April 28 - May 1, 1996.

[0037] The receiver of the invention can operate with any spreading sequence that meets with the specified condition. In many practical systems (satellite or terrestrial), use is made of sequences composed of a unique internal sequence for each channel within a sector or beam (sequence Walsh-Hadamard or Gold) and an external sequence having the same timing and the same length as those of the internal sequence (pseudo-noise). Using a second slow external sequence with a pulse duration equal to the symbol duration permits to solve very large delay differences necessary for instance in case of combination of signals from different satellites. The length of this sequence is an integer multiple of the symbol duration.

[0038] The complex response vector h_1 is designed so as to minimize the mean square error (MSE) between the actual detector output and the output in the absence of noise and interference (MAI). The detector is made adaptive through a simple stochastic gradient algorithm thereby to find the solution to the minimization problem stated above. More sophisticated adaptation algorithms such as the so-called Recursive Least Squares (RLS) or variations thereof

can be used to enhance speed convergence with the penalty of higher complexity.

[0039] Fig. 2 shows an example of possible implementation for the adaptive receiver structure according to the invention Dual BPSK/SS signals. After baseband translation, the received signal $y(m)$ enters a chip matched filter (CMF) 21 and is then sampled every T_c seconds in sampler 22. The complex baseband samples enter then the P-Q C-BAID detectors 23. Each of them processes one of the two D-BPSK/SS signal components and delivers the complex baseband samples at symbol rate on lines 201 and 202 respectively. Processing in the detectors 23 occurs using a user replica code sequence generated by a replica code generator 24. Phase and frequency error detectors 25 and 26 operate at the rate $2/T_c$ to estimate the phase and frequency to remove the frequency offset and phase residual error prior to final P-Q soft sample delivery.

[0040] Thanks to the rotationally phase invariance of the C-BAID detector 23 of the invention, frequency and phase errors can be estimated after despreading and adaptation, thus at a SNR typical for digital communication systems. The implementation shown in Fig. 2 is provided for coherent detection.

[0041] The rotationally phase invariance allows the demodulator to be simplified by providing a simple differential symbol detector at the output of the C-BAID detectors 23.

[0042] Despite the C-BAID rotationally phase invariance, the carrier frequency estimation needs to be quite accurate. However, this is not a major drawback as accurate frequency estimate is required anyway for initial signal acquisition. In order to avoid that the frequency error at the C-BAID input exceeds the tolerable range, the receiver includes a long frequency control loop 203 that receives the frequency error signal $\Delta\omega$ at 27 and uses it to control the downconverter oscillator 30.

The phase error signal $\Delta\phi$ at an output 28 of the detectors 26 is applied to phase rotators 29 for the samples of channels P and Q. The in-phase modulated symbols $b^P(1)$ and the in-quadrature modulated symbols $b^Q(2)$ are delivered at outputs 210 and 220, respectively.

[0043] As mentioned earlier herein, the correlation processing according to the invention uses a signature sequence comprised of a fixed term (replica code) and an adaptive part (auxiliary code) that is updated at least symbol per symbol. This updating and the auxiliary code despreading can be performed in a simple digital circuit comprising a feedback loop with a sampling clock equal to the signal clock. Fig. 3 shows a functional block diagram of a possible implementation for the C-BAID detector 23. The correlation is performed in two blocks 31 and 32 that perform multiply and add operations: the first block 31 performs the correlation using the replica code C_1 while the second block 32 performs the correlation using the auxiliary code $X_1(r)$. Updating the auxiliary code requires a correlation loop 301 that is easily implementable by simple digital devices such as multiplier, adder, delay circuit, shift register etc. Of particular interest is the way the adaptive auxiliary code is generated by a simple complex shift register 39 and an adder 37. The blocks denoted by the reference numerals 34, 36 and 38 represent delay circuits (known per se).

[0044] The outputs 310 and 320 from blocks 31 and 32, respectively, are combined in adder 33, the output of which delivers the detected symbol b_1 . It is to be noted in this regard that the correlation processing involves an inherent delay since it is performed on L samples. As a result, while the detector 23 is detecting the symbol $b_1(r)$, its output delivers the preceding symbol $b_1(r-1)$.

[0045] A further interesting feature of the invention is that the response vector $h_1(r)$ of the detector can be extended over an enlarged observation window spanning over the duration of several interfering symbols. This time span increases the detector performance so as to better cope with the asynchronous CDMA interference effects. A three symbol window represents a good trade-off between complexity increase and performance advantage. A shorter window (two symbols) may provide similar performance under certain circumstances, but does not substantially simplify the demodulation hardware described in the following.

[0046] Fig. 4 represents a block diagram of an adaptive receiver for Direct-Sequence (DS) CDMA signals. This receiver comprises three identical detector (EC-BAID) units 43. Each of them operate over an observation window having a three symbol length (3L) with the window being delayed relative to each other and overlapping each other. The diagram of Fig. 4 is pretty similar to that of Fig. 2 except that each detector unit is arranged to perform a three-symbol correlation using an auxiliary code having a 3L length and that the adaptation coefficients are updated after three symbols. The blocks 41 and 42 represent digital delay lines to shift the observation windows. Block 44 represents a time multiplexer running at frequency $1/T_c$. Block 45 represents a PSK demodulator running at the symbol rate. The frequency error signal $\Delta\omega$ is generated in the demodulator and applied to the frequency control loop 303. The reference numerals 21, 24, 25 and 30 denote a base band filter, a replica code generator, a replica code acquisition unit and a down-converter oscillator as shown in the diagram of Fig. 2.

[0047] Fig. 5a illustrates the structure of a detector unit (EC-BAID I) similar to that of Fig. 3. On Fig. 5b there is illustrated another structure for the detector unit (EC-BAID II) that is also similar to the structure shown in Fig. 3, except that it includes a so-called gating function 51 for multiplying the replica code C_1 under control of the symbol clock (running at frequency $1/3LT_c$) and the super-symbol clock (running at frequency $1/3LT_c$).

[0048] In the diagram of Fig. 4, the detector units 43 are independent from each other, that is each of them generates a respective frequency error signal. However, the invention permits to implement a receiver in which the error signals

generated by the different detector units are combined with each other thereby to increase the detection performance. To this end, the arrangements described before herein can be modified as illustrated in Figs. 6 and 7. On Fig. 7a there is shown a structure for the EC-BAID I detector unit; and Fig. 7b shows a structure for the EC-BAID II detector unit. In this case the adaptive receiver comprises a unique external auxiliary code generator 64 that is completely updated after every symbol interval. The frequency error signal on line 203 for the generation of the auxiliary code is produced by coherent combination of the three individual error signals delivered at the output of the digital delay lines 65, 66 and 67. The reference numerals 41, 42, 61 and 62 also denote digital delay lines.

[0049] The fact that the EC-BAID I embodiment (see Fig. 5a) imposes the orthogonality condition for the auxiliary code x_e with respect to the anchor c_1 is due to the fact that in this way the demodulator is robust vis-a-vis non perfect randomness of the modulating data pattern. It can be seen that the EC-BAID II performance is degraded when the data pattern is partly non random. The EC-BAID II does not work at all in case of an unmodulated ODMA signal whilst EC-BAID I works perfectly in all conditions with insignificant performance loss compared to EC-BAID II even for pure random data.

15 The Select and Add Architecture

[0050] Another possible EC-BAID option has been dubbed Select and Add. As depicted in Fig. 8, the correlations $y(r).c_1$ and $x_e^e . c_1$, respectively, are evaluated in blocks 81 and 82 to yield the $b_1(r)$ output at symbol rate. The vector x_e^e is stored in memory 86 and each of its 3L elements is updated every $T_c/3$; in particular, during the i -th chip period within the r -th symbol period, the coefficients of x_1 relevant to the i -th chip of $y(r=1)$, $y(r)$ and $y(r+1)$ are updated. The most recent 3L input chips are stored in memory 87. Multiplexers 85 and 88 properly re-align internal dataflow, while multiplexer 83 selects the desired EC-BAID algorithm version between I and II. More precisely for EC-BAID II the switch is set to zero for the external chunks of the error signal as the anchor orthogonality condition shall only be imposed for the central (useful) symbol. The AGC 84 on the feedback loop is needed in order to keep the b_1 value constant for the error signal generation at different SNIR operating conditions.

[0051] The main difference between S&A and O&A resides in the evaluation of the error signals and in the relevant update of the adaptive vector x_1 : in the O&A groups of three error contributions are summed every $3T_s$ while in the S&A each error contribution is summed separately every T_s and this way numerical results and convergence speed are almost equal.

[0052] The S&A architecture is characterized by a considerable hardware complexity saving in terms of both arithmetical elements and memory cells at the expenses of an increased clock rate.

[0053] In fact, for O&A proper operations three distinct arithmetical units are required and moreover, in addition to the memory capacity for x_1 , some extra delays (memory elements) are needed to provide proper timing between signals of the various circuit branches.

[0054] On the other hand, in the S&A version only one arithmetical unit is needed due to hardware multiplexing, and moreover only vector x_1 and 3L input (y^e) samples have to be stored, so that the extra delay blocks previously mentioned are no longer needed.

[0055] These modifications, without affecting overall performances, allow for nearly 50 % gate complexity saving and for nearly 70 % RAM capacity saving at the expenses of a three times higher clock rate. The latter may limit the S&A applicability to very high chip rate applications.

Error signal truncation effects

[0056] The S&A fixed-point ASIC implementation inevitably introduces some truncation errors with respect to theory which is based on floating-point arithmetic. For this particular adaptive architecture, which is based on a feedback loop, these quantization errors may have dramatic effects on the overall algorithm convergence. In particular, because of this quantizations, the error contributions which are used to generate the adaptation vector x_1 may be not perfectly orthogonal to the code sequence vector; this situation has to be avoided because the algorithm is able to maintain a stable steady-state value only for the x_1 component which is orthogonal to c_1^e (it is referred here to the EC-BAID II, but the same considerations are valid for the EC-BAID I, taking into account that in that case the algorithm controls the $x_{1,\omega}$ components orthogonal to c_1^e). On the other hand, if the finite arithmetic effects generate a x_e^e component which is not orthogonal to c_1^e , this one may indefinitely increase without being controlled by the algorithm, and thus causing failure of the whole system.

[0057] In order to avoid this situation it is sufficient to calculate the error signal e_1 (based on the quantized y^e and b_1 values as shown here below with full-precision arithmetic).

$$E_1 \Delta b_1 [y^{c_1}(\tau) \cdot \frac{y^{c_1}(\tau)^T \cdot c_1}{L} c_1];$$

[0058] The error signal generated this way is perfectly orthogonal to c_1 thus preventing the aforesaid problem. This means that, starting from quantized y^e and b_1 values, the processing relevant to e_1 (and so x^{e_1}) is performed with an internal word-length suitable for whole signal dynamics, so that no further truncation is introduced. Fig. 9 shows that the BER system performances (for $L = 128$, $N = 64$, $E_b/N_0 = 6$ dB and $C/I = -6$ dB) when no further truncation is introduced in the evaluation of e^{e_1} (and so x^{e_1}) as well as when there is just 1-bit error in the internal word-length dimensioning.

[0059] Finite arithmetic effects on all the other S&A circuit internal signals can be regarded as additional noise without causing any problem to the algorithm convergence towards the steady-state vector $x^{e_{1,opt}}$. Although this solution has been exemplified for the S&A case its applicability can be more general.

The Linear Combiner Architecture

[0060] The linear combiner (LC) architecture represents a hybrid solution between the Baseline and the OA/SA. The main feature is that while there are three separate auxiliary sequences for each EC-BAID detector. Differently from the baseline EC-BAID, the error signals are linearly combined so that the three detectors are not anymore disjoint. By doing so the convergence speed is improved compared to the baseline although the complexity advantage shown by the S&A is not achieved. Details about the combination law are provided in next Section herein. The LC EC-BAID top level architecture is shown in Fig. 10.

EC-BAID Architectures Summary

Baseline Architecture

[0061] As shown in the previous paragraphs, the EC-BAID I and EC-BAID II versions formerly proposed require three separate units, each with its own local copy of x_1 ; the first unit processes symbol periods $(r-1)$ -th, r -th and $(r+1)$ -th to produce the output $b_1(\tau)$, the second one processes symbol periods (r) -th, $(r+1)$ -th and $(r+2)$ -th to produce the output $b_1(r+1)$, and the third unit operates likewise to produce $b_1(r+2)$. This way, the generic n unit includes its own $x^{e,n}_1$ local vector, to be updated every $3T$ with the contribution (error signal) of its own output. The equations for this baseline architecture are summarized in Table 1.

EC-BAID-I and EC-BAID-II, Baseline output construction :

$$b_1(3s+n-1) = - \sum_{L=1}^L h_1(s)^T \cdot y^c(3s+n-1) \text{ with } h_1(s) = x_1(s) + c_1$$

with n EC-BAID detector index

EC-BAID-I, updating of vectors x_1

$$x_{1,w}(s+1) = x_{1,w}(s) - \gamma e_{1,w}(s) \quad w = -1, 0, 1$$

$$e_{1,w}(s) \triangleq b_1(3s+n-1) [y_w(3s+n-1) - \frac{Y_w(3s+n-1)^T \cdot c_1}{L} c_1] \quad w = -1, 0, 1$$

with s super-symbol index

EC-BAID-II, updating of vectors x_1

$$x_1(s+1) = x_1(s) - \gamma e_1(s)$$

$$e_1(s) \triangleq b_1(3s+n-1) [y^{sm}(3s+n-1) - \frac{y^{sm}(3s+n-1)^T \cdot c_1}{L} c_1]$$

with s super-symbol index

with

$$c_1 \triangleq \begin{matrix} 0 & x_{1,-1}(s) & e_{1,-1}(s) \\ c_1 & x_{1,0}(s) & e_{1,0}(s) \\ 0 & x_{1,1}(s) & e_{1,1}(s) \end{matrix}, \quad x_1(s) \triangleq \begin{matrix} x_{1,-1}(s) \\ x_{1,0}(s) \\ x_{1,1}(s) \end{matrix}, \quad e_1(s) \triangleq \begin{matrix} e_{1,-1}(s) \\ e_{1,0}(s) \\ e_{1,1}(s) \end{matrix}$$

Table 1 : Equation EC-BAID-I and EC-BAID-II for the baseline architecture

"Overlap and Add" Architecture

[0062] The EC-BAID-I and EC-BAID-II versions named "Overlap and Add" (O&A), bring about an improvement : the three units still process input data symbols producing in turn the desired output b_1 , this time using a unique vector x_1 . This vector is now updated with the sum of the three unit contributions (sum of the three error signals), still every $3T_s$.

[0063] The advantage obtained from this architecture is twofold : from the area saving point of view now only one vector x_1 rather than 3 need to be stored, while from the speed performance point of view the x_1 updating is three times faster, since every $3T$, three contributions are summed together instead of one only. The equations for the O&A architecture are summarized in Table 2.

EC-BAID-I and EC-BAID-II, Overlap and Add-output construction :

$$b_1(3s+n-1) = - \frac{1}{L} h_1(s)^T \cdot y^c(3s+n-1) \text{ with } h_1(s) = x_1(s) + c_1$$

with n EC-BAID detector index

EC-BAID-I Overlap and Add, updating of vectors x_1

$$x_{1,w}(s+1) = x_{1,w}(s) - \gamma [e_{1,w}(s-1) + e_{1,w}(s-1)] \quad w = -1, 0, 1$$

$$e_{1,w}(s) \triangleq b_1(3s+n-1) [y_w(3s+n-1) - \frac{y_w(3s+n-1)^T \cdot c_1}{L} C_1] \quad w = -1, 0, 1$$

with s super-symbol index

EC-BAID-II Overlap and Add, updating of vectors x_1

$$x_1(s+1) = x_1(s) - \gamma [e_{1,-1}(s-1) + e_{1,0}(s-1) + e_{1,1}(s-1)]$$

$$e_{1,w}(s) \triangleq b_1(3s+n-1) [y^{sm}(3s+n-1) - \frac{y^{sm}(3s+n-1)^T \cdot c_1}{L} C_1]$$

with s super-symbol index

with

$$\begin{array}{ccccc} 0 & & x_{1,-1}(s) & & e_{1,-1}(s) \\ c_1 \triangleq c_1 & , & x_1(s) \triangleq x_{1,0}(s) & , & e_{1,0}(s) \\ 0 & & x_{1,1}(s) & & e_{1,1}(s) \end{array}$$

Table 2 : Equations EC-BAID-I and EC-BAID-II for the "Overlap and Add" architecture

"Select and Add" Architecture

[0064] The final architecture proposed to implement EC-BAID-I and EC-BAID-II, named "Select and Add" (S&A) provides the same bit error rate and convergence speed performances of the O&A one but it allows a considerable circuit complexity reduction.

[0065] Its top level block diagram shown in Fig. 2, where the detector unit is now the one sketched in Fig. 8.

[0066] The S&A architecture exploits the possibility of using a clock of period $T_c/3$ so that it is possible to use the arithmetical part of the circuit three times for each T_c ; this allows to calculate the output $b_1(\tau)$ in one period T_s , getting the entire product $x_{e_1}^T(\tau) \cdot c_{e_1}$ and to update, in the same period T_s , the whole vector x_{e_1} with the 3L error signal coefficients. The equations for the S&A architecture are shown in Table 3.

The Linear Combiner Architecture

[0067] The linear combiner (LC) architecture represents a hybrid solution between the Baseline and the OA/SA. The main feature is that while there are three separate auxiliary sequences for each EC-BAID detector they interact through a linear combination of the respective error signals as it has been discussed before.

[0068] The functioning of the LC EC-BAID is regulated by the set of equations shown in Table 4 :

EC-BAID-I and EC-BAID-II, Select and Add - output construction:

$$b_1(r) = - \sum_{l=1}^L h_l(r)^T \cdot y^e(r) \quad \text{with } h_l(r) = x_l(r) + c_l$$

EC-BAID-I Select and Add, updating of vectors x_1

$$x_{1,w}(r+1) = x_{1,w}(r) - \gamma e_{1,w}(r-1) \quad w = -1, 0, 1$$

$$e_{1,w}(r) \triangleq b_1(r) [y_w(r) - \frac{y_w(r)^T \cdot c_1}{L} c_1] \quad w = -1, 0, 1$$

with r symbol index

EC-BAID-II Select and Add, updating of vectors x_1

$$x_1(r+1) = x_1(r) - \gamma e_1(r-1)$$

$$e_1(r) \triangleq b_1(r) [y^{an}(r) - \frac{y^{an}(r)^T \cdot c_1}{L} c_1]$$

with r symbol index

with

$$c_1 \triangleq \begin{matrix} 0 & x_{1,-1}(r) & e_{1,-1}(r) \\ c_1 & , x_1(r) \triangleq x_{1,0}(r) & , e_1(r) \triangleq e_{1,0}(r) \\ 0 & x_{1,1}(r) & e_{1,1}(r) \end{matrix}$$

Table 3 : Equations EC-BAID-I and EC-BAID-II for the "Select and Add" architecture

EC-BAID-I and EC-BAID-II, Overlap and Add-output construction:

$$b_1(3s+n-1) = - \sum_{L=1}^L h_1(s)^T \cdot y^L(3s+n-1) \text{ with } h_1(s) = x_1(s) + c_1$$

with n EC-BAID detector index

EC-BAID-I Linear Combiner, updating of vectors x_1

$$x_{1,w}(s+1) = x_{1,w}(s) - \gamma[e_{1,w}(s) + e_{1,w}(s) + e_{1,w}(s)]$$

$$x_{1,w}(s+1) = x_{1,w}(s) - \gamma[e_{1,w}(s+1) + e_{1,w}(s) + e_{1,w}(s)] \quad w = -1, 0, 1$$

$$x_{1,w}(s+1) = x_{1,w}(s) - \gamma[e_{1,w}(s+1) + e_{1,w}(s+1) + e_{1,w}(s)]$$

$$e_{1,w}(s) \triangleq b_1(3s+n-1)[y_w(3s+n-1) - \frac{y_w(3s+n-1)^T \cdot c_1}{L} c_1] \quad w = -1, 0, 1$$

with s super-symbol index

EC-BAID-II Linear Combiner, updating of vectors x_1

$$x_1(s+1) = x_1(s) - \gamma[e_1(s) + e_1(s) + e_1(s)]$$

$$x_1(s+1) = x_1(s) - \gamma[e_1(s+1) + e_1(s) + e_1(s)]$$

$$x_1(s+1) = x_1(s) - \gamma[e_1(s+1) + e_1(s+1) + e_1(s)]$$

$$e_1(s) \triangleq b_1(3s+n-1)[y^{an}(3s+n-1) - \frac{y^{an}(3s+n-1)^T \cdot c_1}{L} c_1]$$

with s super-symbol index

with

$$c_1 \triangleq \begin{bmatrix} 0 & x_{1,-1}(s) & e_{1,-1}(s) \\ c_1 & x_1(s) & e_1(s) \\ 0 & x_{1,1}(s) & e_{1,1}(s) \end{bmatrix}$$

Table 4 : Equations EC-BAID-I and EC-BAID-II for the "Linear Combiner" architecture (n=1,2,3)

[0069] Numerical results, both analytical and computer simulated, show excellent and fast convergence of the adaptation algorithm and further show an independence of the mean square error to the phase offset of interfering signals. This convergence of the adaptation algorithm is illustrated by the diagram of Fig. 11 that shows the convergence of the real and imaginary parts of the adaptation coefficients in the conditions specified.

[0070] It should also be mentioned that the blind adaptive receiver of the invention shows an excellent robustness to the residual frequency errors of the useful signal at the input of the detector unit. This result is obtained thanks to the use of complex coefficients for the auxiliary code as outlined earlier herein. This behaviour of the receiver of the invention is illustrated by the diagram of Fig. 12 showing the variation of the binary error rate BER versus the adaptation

factor γ for various detector types.

[0071] One of the strong points in favor of the (E)C-BAID characterized by complex-valued coefficients, is related to its performance independence to interferers frequency offset. Such same frequency offset can be quite large compared to the symbol rate in a Low- or Medium Earth Orbiting (LEO-MEO) satellite constellation. This important (E)C-BAID feature is testified by Fig. 13.

[0072] The diagram of Fig. 13 shows that the receiver according to this invention is more robust to frequency error than a known DA-MMSE detector. More particularly, the robustness to frequency error of the receiver of the invention is three orders of magnitude larger than that of the DA-MMSE schema. It is thus compatible to the typical standard deviation of the frequency estimator 25. In addition, cancellation of MAI expressed in terms of BER is comparable to the performance of the data-aided adaptive processing.

[0073] With regard to the number of interfering users, the binary error rate (BER) of the detector of the invention reveals that the extended version of the detector makes it possible to increase substantially the number of interfering users even in cases where the useful signal power is substantially lower than the interfering signal power. This is exemplified in the case of a satellite communication system in which the power control is slow because of the propagation time. This performance is comparable to that of the conventional data-aided adaptive detector (EDA-MSE) as shown by Fig. 14 which represents the BER versus the number of interfering signals for various adaptive detector types with different values of the ratio E_p/N_0 between the average received energy of the quaternary constellation symbols at the edge of the beam and the one-sided AWGN (additional white gaussian noise) power spectral density N_0 .

[0074] The impact of the different types of detectors is well evidenced looking at the statistical distribution of the total signal-to-noise plus interference ratio at the detector output p .

[0075] Fig. 15 shows the probability density function (PDF) of p for the CR, the C-BAID and the EC-BAID, with random interferers' carrier phases and code delays. It is fairly evident that the values of p at the output of the CR show a relatively large dispersion, due to the different delay and carrier phase assignments on the active channels. The effect of the interference-mitigating detectors is twofold: first, they obviously increase the mean value of p so as to improve the mean quality of the link; second, they reduce the dispersion of p around its mean value, and this has the effect of reducing the outage probability of the link. CDMA with long codes (sometimes referred to as Random CDMA) is not easily amenable to interference mitigation, but the use of long codes causes a sort of symbol-by-symbol randomization of MAI to pursue the same goal of outage probability reduction. This attractive feature of the EC-BAID is further enhanced by the use of d-BPSK or QPSK-RS signal formats. Figure 11b shows simulation results for the pdf of p in the case of BPSK, d-BPSK and QPSK. The occupied bandwidth being equal, d-BPSK/QPSK-RS allow the use of signature sequences having a repetition period which is twice that of the codes of the BPSK-RS or BPSK-CS cases, thus doubling the codebook size (i.e., the maximum number of sequences available) and yielding a twofold capacity. Also, looking at Fig. 11b it can be observed that, though BPSK and d-BPSK have similar average value of p , the variance is reduced for the latter. Compared to d-BPSK, QPSK-RS provides a further advantage in terms of average p increase and σ_p reduction due to the fact that each signal employs one and not two spreading sequences as for d-BPSK. Although immaterial to the CR, this feature reduces the number of space dimensions occupied by the CDMA multiplex, thus enhancing EC-BAID interference mitigation capabilities. The QPSK-RS advantage versus BPSK-RS and d-BPSK in terms of BER for a typical (fixed) interferer delay distribution is shown in Fig. 16. Monte Carlo BER simulations results utilizing the adaptive LMS version of the EC-BAID described earlier closely follows theoretical predictions.

Fading performance

[0076] Thanks to the previously described (E)C-BAID rotationally phase invariance the proposed detector is capable to provide remarkable performance also over flat fading channels where both useful signal and interferers are affected by independent Rician fading with Rice factor K and the user speed ranges from 5 to 80 mph (assuming a 2 GHz carrier frequency). As the simulation results in Fig. 17 indicate, the EC-BAID provides a remarkable advantage over conventional CDMA correlation receivers even over fast fading channels. In all simulations the useful signal carrier phase has been (ideally) estimated after the EC-BAID. Although results in Fig. 17 assume ideal channel estimation, similar performance have been achieved using pilot-aided channel estimation techniques.

[0077] It was observed that simulation results remain practically unchanged by removing the fading on the CDMA interferers even in the case of fast fading. This fact demonstrates that the EC-BAID is intrinsically robust with respect to MAI flat fading fluctuations (both in amplitude and phase).

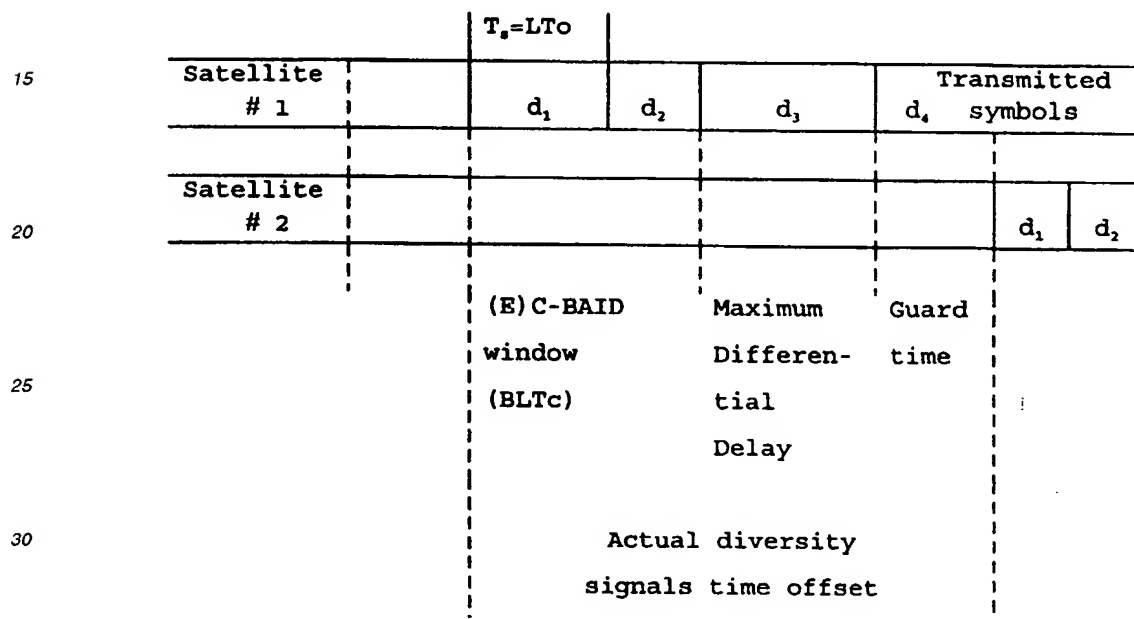
Satellite Path Diversity

[0078] The proposed (E)C-BAID detector is also well suited for mobile satellite systems whereby satellite path diversity is adopted. In this case typically a different spreading sequence is used for each forward link satellite signal although the information bits transmitted are the same. In this case the Fig. 18 block diagram is modified as shown in

the following Fig. 15 whereby a same EC-BAID possible implementation is exemplified. Assuming that up to N_D diversity order is implemented in the system, then the demodulator is composed by N_D EC-BAID each of them dealing with the signal coming from a different satellite. Because of the different satellite geometry independent fine frequency and time/phase adjustment is required on each finger. In case the differential delay exceeds the symbol duration (as it is often the case), the delay ambiguity shall be resolved by means of external and as frame unique word not shown here for simplicity.

[0079] In any case, the symbols of the same data stream transmitted by different satellites shall be spread by a satellite and user unique sequence and staggered in time so that there is no possibility that the same symbol (± 1 symbol in case of EC-BAID) overlaps with another diversity path coming from a different satellite.

[0080] This staggering technique is illustrated as follows :



This modulator symbol staggering is then recovered at the demodulator side by means of the delay adjustment block shown in Fig. 18. This is a very important technique to avoid possible destructive interaction between useful and diversity path within each EC-BAID demodulator.

EC-BAID Application to Multi-rate CDMA

[0081] All of the third-generation standards for wireless CDMA, be it terrestrial or satellite, encompass a multi-rate access capability to accommodate best multimedia services. All rates are integer submultiples of a maximum rate R_M (e.g., 2048 or 384 kb/s) which depends on the kind of terminal (fixed, indoor, fully mobile etc.) and network (indoor, outdoor terrestrial, satellite etc.). Usually, but not necessarily, the supported bit rates are in the form $R_{ck} = R_M/2^k$ just for easier implementation and coordination, while of course the chip rate R_c is invariably the same on every allocated channel.

[0082] A recently proposed approach to deal with multi-rate CDMA makes use of the so-called OVFS codes. The OVFS codes family is just a re-labeled version of the popular set of Walsh-Hadamard codes class, wherein the re-labeling is the value-added feature. Specifically, the OVFS are a re-organization of the Walsh code in layers. The codes on each layer have twice the length of the codes in the layer above. Also, the codes are organized in a tree, wherein any two "children" codes on the layer underneath a "parent" code are generated by repetition and repetition with sign change, respectively. The peculiarity of the tree is that a pair of codes is not only orthogonal within each layer (each layer is just the complete set of the Walsh codes of the corresponding length), but they are also orthogonal between layers, provided that the shorter one is not an ancestor of the longer.

[0083] Fig. 19 illustrates an example of two user signals with different bit rates, namely, rate R_o for the upper signal and rate $4R_o$ for the lower one. The former uses the length-8 code $C_3(3)$, while the latter employs the length-2 code $C_1(1)$. The two codes are orthogonal since the longer one does not have $C_1(1)$ as ancestor at layer 1. Therefore, a conventional correlation receiver suffers from no MAI either when used on signal 1 or on signal 2.

[0084] Let us first examine the demodulator for the "slow" signal # 1. It can be said that the interference generated by signal # 2, when observed on a "long" symbol time $T_o = 1/R_o$ bears a time-varying spreading code. Assuming $a_2(1)$ as the "reference" symbol, signal # 2 can be fictitiously seen as a rate R_o DS-SS signal transmitting just $a_2(1)$, and with a spreading code equal to

$$[C_1(1) \quad a_2(1)a_2(2)C_1(1) \quad a_2(1)a_2(3)C_1(1) \quad a_2(1)a_2(4)C_1(1)]$$

[0085] Of course, if the signals are synchronous, they stay orthogonal by virtue of the OVFS codes properties, and the time-variance described above is immaterial. An issue may arise when the multi-rate MAI is asynchronous to the desired user (i.e., inter-cell or inter-beam interference in the forward link). Assume now we have to demodulate the generic user signal # m in a CDMA network with asynchronous interference (either intra-cell/beam MAI in the uplink or inter-cell/beam MAI in the downlink), and assume also that it has the highest rate allowed in the network. The relation of all interfering signal to signal # m will be similar to that of signal # 1 to signal # 2, all of them will bear a longer code than user # m. The latter will therefore see a set of cyclically varying spreading codes on the interfering signal, whose repetition period will be in general the ratio M between the highest and the lowest bit rate allowed in the network. The interference-mitigating detector for user # m can therefore be designed exploiting this cyclical regularity. It will be made of a bank of M conventional EC-BAIDs which are cyclically operated every M symbol periods, in such a way that everyone always sees the same (sub-)code on the interfering signals. The outputs of such detectors are then "demultiplexed" so as to give a symbol-rate stream for subsequent processing.

[0086] The situation is different if user # m has the lowest rate in the network (basic rate). In such condition, it sees a random-varying set of codes, according to the remarks in sect. 2. The solution to this case can be found as follows. What we should have to do now is to split the symbol interval into M sub-intervals and to use again M different EC-BAIDs each matched to the relevant sub-code, and each yielding a different soft-valued output. With this partitioning, each detector would see now the same code from symbol interval to symbol interval, and would follow a conventional design. The M soft outputs of such detectors would then be combined to yield the final decision variable. This sort of multi-layer linear detector is fully equivalent to a linear detector for the whole length of the symbol. Therefore, the conclusion is that a conventional EC-BAID with no modifications can be used in the case of a lower-rate signal.

Of course, if user # m has an intermediate bit-rate, the only parameter that matters is the ratio between its actual rate and the maximum rate in the net to build-up a multi-rate architecture as in Fig. 3. This figure represents the functional block diagram for a user operating at a bit rate M times the basic system bit rate $1/T_h$. As it is apparent from the previous discussion, the basic EC-BAID detectors can be reused although each of the M parallel detectors operates on a disjoint short symbol. The M detectors are sequentially activated with periodicity T_h^{slow} and a duty cycle T_h^{slow}/M . This can be easily achieved distributing the clocks with a selector similar to the one distributing the input samples to the different EC-BAID detectors. The output soft samples at rate $1/T_h^{fast} M/T_h^{slow}$ are sequentially obtained from detectors 1 to M. The selection procedure is then repeated at the end of each period T_h^{slow} . It is also evident that the number of EC-BAID detectors required to a specific demodulator operating in a CDMA environment employing OVFS multi-rate techniques is proportional to the ratio between the maximum rate supported with respect to the basic network rate. The additional demodulator complexity is therefore only impacting users transmitting at higher rates than the basic one.

EC-BAID Applicability to Quasi-Random CDMA

[0087] One of the major disadvantages of symbol length sequences is that in case of a stationary system geometry the cross-correlation between two signals will repeat on a symbol-by-symbol basis thus penalizing users experiencing worst-case cross-correlation. A technique commonly adopted to overcome this problem in many practical systems consists of overlaying the user specific channelization sequence with a PN scrambling sequence having duration equal to an integer multiple (m) of the symbol duration. By doing so the cross-correlation is changing on a symbol-by-symbol basis and repeats after m symbols. This is the approach that has been adopted for the CDMA cellular Telecommunication Industry Association (TIA) standard IS-95 and the third generation (IMT-2000) Wideband CDMA proposed by several countries to the International Telecommunication Union (ITU).

[0088] It is easy to see that this quasi-random CDMA (a true random CDMA system will have a PN sequence never repeating) can be reconducted to the multi-rate case whereby a bank of (E)C-BAID detectors working in parallel but with duty cycle staggered in time, will make possible to cope with the symbol-by-symbol varying spreading sequence. It is easy to show that the random CDMA performances can be closely achieved with a relative little value for m (e.g. 4, 8) thus with an affordable demodulator complexity.

EC-BAID Applicability to Frequency Selective Channels

[0089] In case of terrestrial mobile communications (especially in urban areas), the radio channel can not be considered frequency-flat any longer, due to the large number of propagation paths generated by multiple signal reflections on buildings that cause the propagation channel to be frequency-selective. As a consequence, in this case, the BAID algorithm, that was designed for a non frequency-distorting channel, may not work properly anymore. Therefore, the BAID algorithm must be suitably modified in order to retain its effectiveness in combating the MAI also in this harsh propagation scenario. For the sake of simplicity, in the following we will refer to a two-ray only propagation channel (i.e. direct plus reflected path), but the extension of the relevant concepts to a more involved case of multipath propagation with more than two rays is rather straightforward. Under these assumptions, the complex array of the received signal samples $w(r)$ can be expressed as follows :

$$w(r) = y(r) + \alpha y'(r),$$

where

$y(r)$ is the complex array of the signal samples we would receive in the absence of multipath propagation (direct path);

$y'(r)$ is the complex array of the delayed signal samples (reflected path);

α is a complex coefficient representing the attenuation of the delayed path.

[0090] From the detector standpoint, the useful (i.e. the information-bearing) part of the received signal appears as it be spread by a modified signature code s_1 that can be expressed as follows :

$$s_1 = c_1 + \alpha c_1,$$

where assuming for simplicity of illustration that the multipath is delayed by an integer number of chips we can write :

$$c_1 = [0, 0, \dots, 0, \overset{m}{c_{1,1}}, \overset{L=m}{c_{1,2}}, \dots, c_{1,L-m}]$$

represents the delayed (truncated) replica of the spreading code, and m is the time delay, normalized with respect to the chip interval T_c . This fact causes a sort of 'mismatching' between the actual received signature s_1 and the expected nominal code c_1 . As matter of fact, the BAID algorithm updates the adaptive part of the detector coefficients $x_1(r)$ on the basis of the 'anchoring' condition $c_1 \cdot x_1(r) = 0$ that leads to the cancellation of every signal belonging to the subspace orthogonal to c_1 . Unfortunately, in the case of multipath propagation (or any other mismatch condition caused for example by incorrect input signal sampling), the a-priori anchor c_1 does not match anymore the input signal, and it is thus causes useful signal partial cancellation. This leads to a waste of useful signal power, that definitely causes the algorithm to become less efficient as the contribution of the delayed path gets larger (i.e. for increasing α).

[0091] This inconvenience can be circumvented by resorting to a simple modification of the original BAID algorithm. In particular, the coefficients of the detector $h_1(r)$ shall now be expressed as :

$$h_1(r) = s_1 + x_1(r),$$

and the 'anchoring' condition shall be modified accordingly : $s_1 \cdot x_1(r) = 0$. The new version of the algorithm thus becomes :

$$x_{1,i}(r) = x_{1,i}(r-1) - \gamma b_1(r-2) [y_i(r-2) - b_1(r-2) s_{1,i}]$$

where :

$$b_1(r) = \frac{h^T \cdot y(r)}{|s_1|^2}, \quad b_1(r) = \frac{(s_1) \cdot y(r)}{|s_1|^2}$$

and $x_{1,1}(r)$, $y_1(r)$, $s_{1,1}$ are the i -th elements of $x_1(r)$, $y(r)$ and s_1 , respectively. The above modified algorithm allows to not only avoid the destructive multipath effects on the EC-BAID detector but also to coherently combine the multipath energy avoiding the use of the rake demodulator typically used in conventional CDMA detectors operating over frequency selective fading channels.

[0092] Essentially, in the modified algorithm, the nominal signature code c_1 shall be replaced by the actual received signature code (or anchor) s_1 , which takes into account for the multipath propagation phenomena. It should be observed that this modified anchor s_1 is not anymore a binary code, as it was the case for the flat fading case, but a complex-valued sequence. The information concerning the channel delay profile, can be obtained from the pilot channel, which in the forward link is a DS/SS-CDMA signal broadcast with very low-rate (or without) data modulation, at a power level typically higher than the traffic channels, by every network's Base Station (BS) as a synchronization aid for the Mobile Terminals (MTs) within the cell's coverage range. Channel estimation can also be easily obtained using pilot symbol periodically inserted in the traffic channel to allow for channel estimation in the presence of smart directive antennas.

[0093] In the reverse link the use of a code division or time multiplexed pilot symbols makes possible to perform data-aided channel estimation similarly to the forward link.

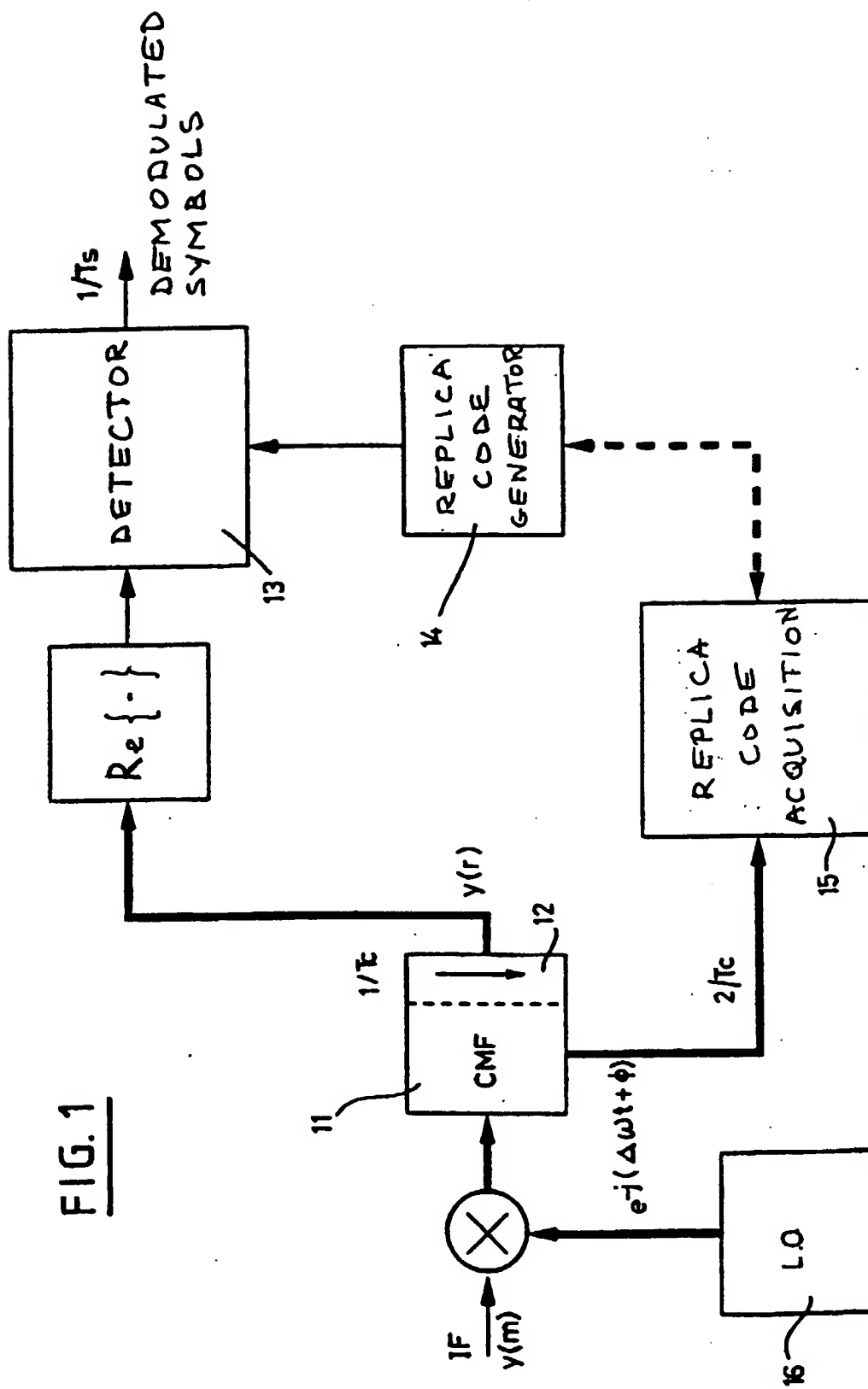
[0094] Numerical results confirm the interference-mitigation capability of the modified BAID algorithm operating in the presence of multipath channel, even with strong reflected signal component (i.e. $|\alpha|=1$), provided that a reliable channel delay profile is made available.

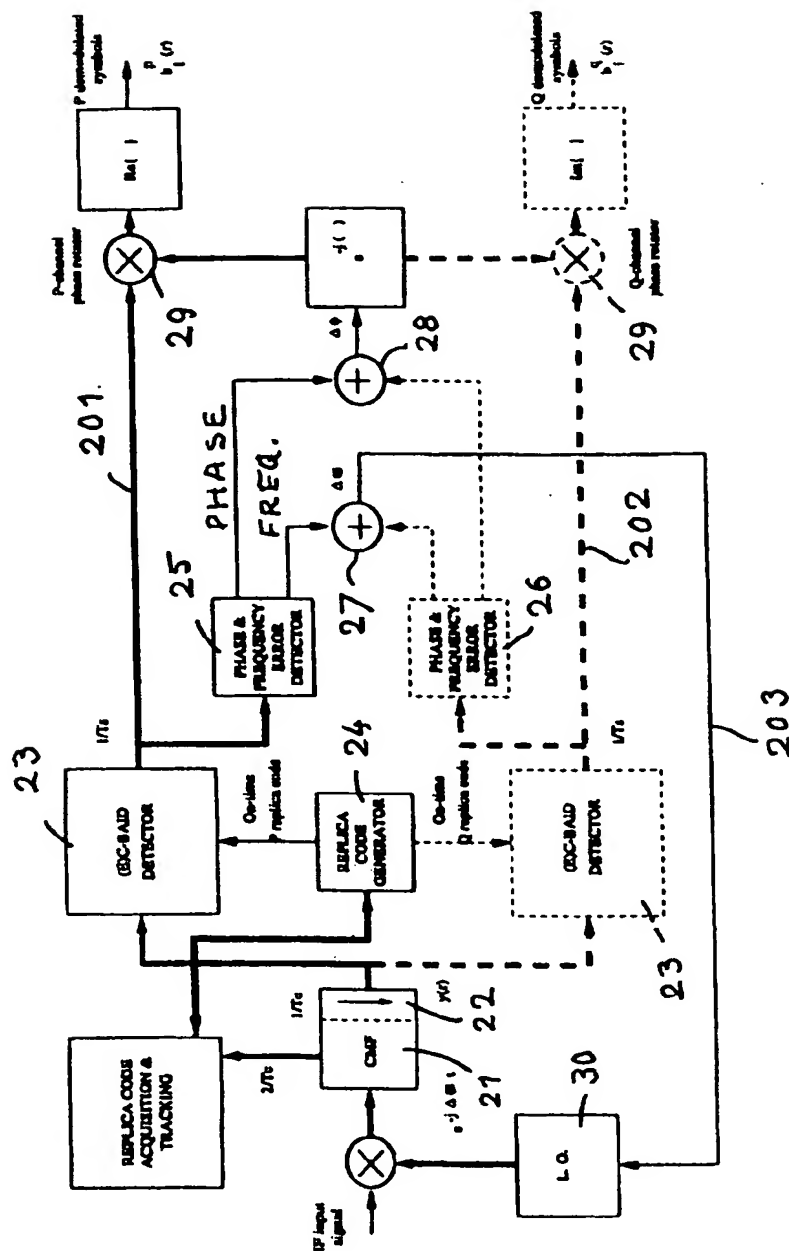
[0095] It is to be understood that the embodiments shown in the drawings are only a few exemplary implementations given to illustrate the way of carrying out the adaptive correlation processing in accordance with the invention.

Claims

1. An adaptive receiver for CDMA signals, comprising a blind adaptive detector for detecting symbols in an incoming data stream using a user signature sequence, wherein said user signature sequence is comprised of a fixed term C_1 and a complex adaptive part $X_1(r)$ having a predetermined length that extends over a number of samples of the incoming data stream, said complex adaptive part being updated automatically and periodically after every interval spanning over one symbol or a plurality of symbols.
2. An adaptive receiver as claimed in claim 1, comprising means to determine said complex adaptive part from the frequency error signal measured on the signal obtained after said symbol detection.
3. An adaptive receiver as claimed in claim 2, wherein said means to determine said complex adaptive part is arranged to minimize the mean square error MSE between the output signal from said detector unit and the data within a predetermined time interval.
4. An adaptive receiver as claimed in claim 2, wherein said means to determine said complex adaptive part comprises a complex shift register and an adder.
5. An adaptive receiver as claimed in claim 1, wherein said adaptive detector comprises at least one detector unit arranged to accept the samples of the incoming data stream within an observation window extending over a predetermined number of samples.
6. An adaptive receiver as claimed in claim 1, wherein said adaptive detector comprises a plurality of detector units, each detector unit being arranged to accept the samples of the incoming data stream within a respective observation window extending over a predetermined number of samples, the observation windows for said plurality of detector units being delayed relative to each other.
7. A blind adaptive detector for detecting symbols in an adaptive receiver for CDMA signals, comprising a blind adaptive detector for detecting symbols in an incoming data stream using a user signature sequence, wherein said user signature sequence is comprised of a fixed term C_1 and a complex adaptive part $X_1(r)$ having a predetermined length that extends over a number of samples of the incoming data stream, said complex adaptive part being updated automatically and periodically after every interval spanning over one symbol or a plurality of symbols.

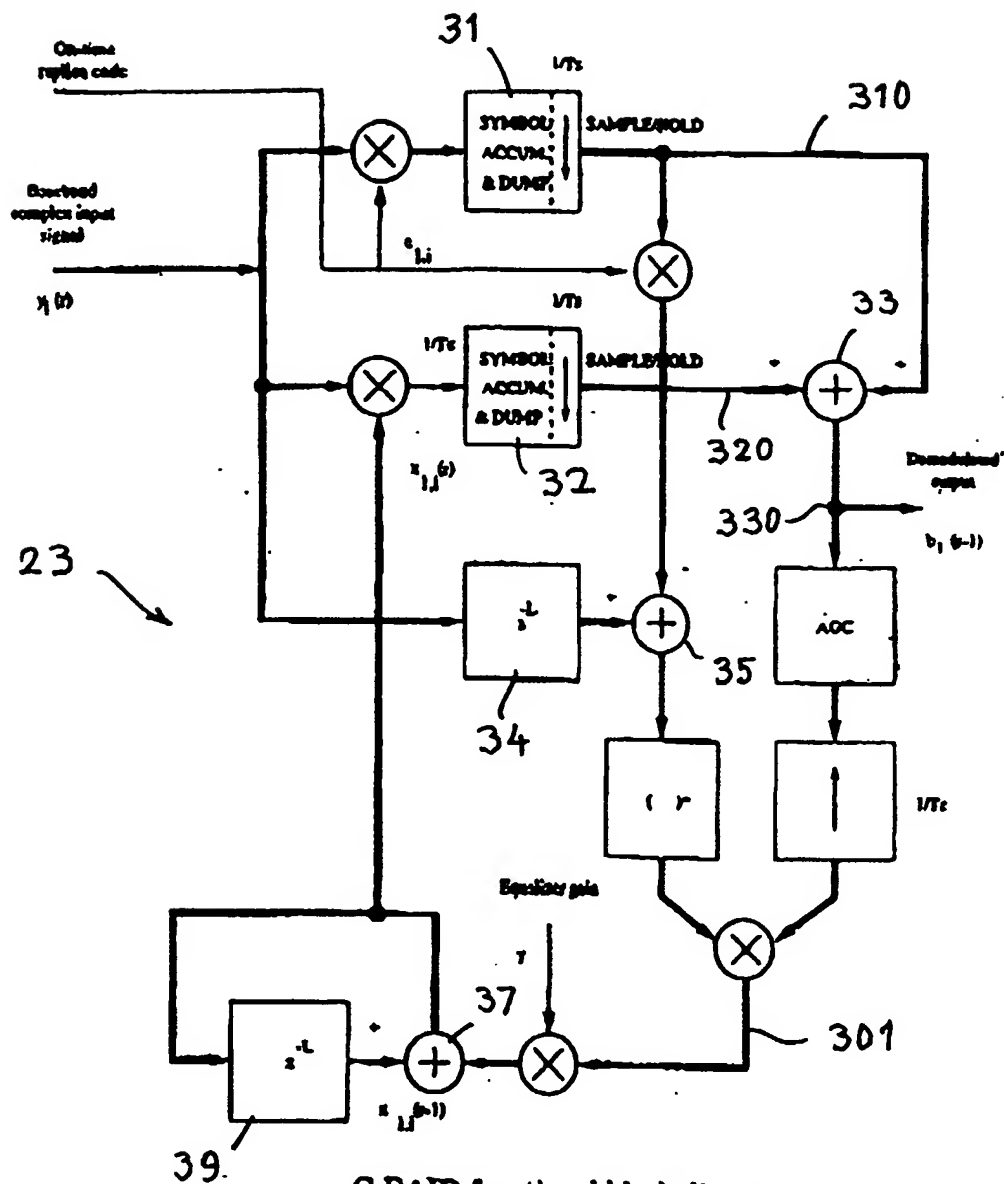
8. A blind adaptive detector as claimed in claim 7, comprising means to determine said complex adaptive part from the frequency error signal measured on the signal obtained after said symbol detection.
9. A blind adaptive detector as claimed in claim 8, wherein said means to determine said complex adaptive part is arranged to minimize the mean square error MSE between the output signal from said detector unit and the data within a predetermined time interval.
10. A blind adaptive detector as claimed in claim 8, wherein said means to determine said complex adaptive part comprises a complex shift register and an adder.





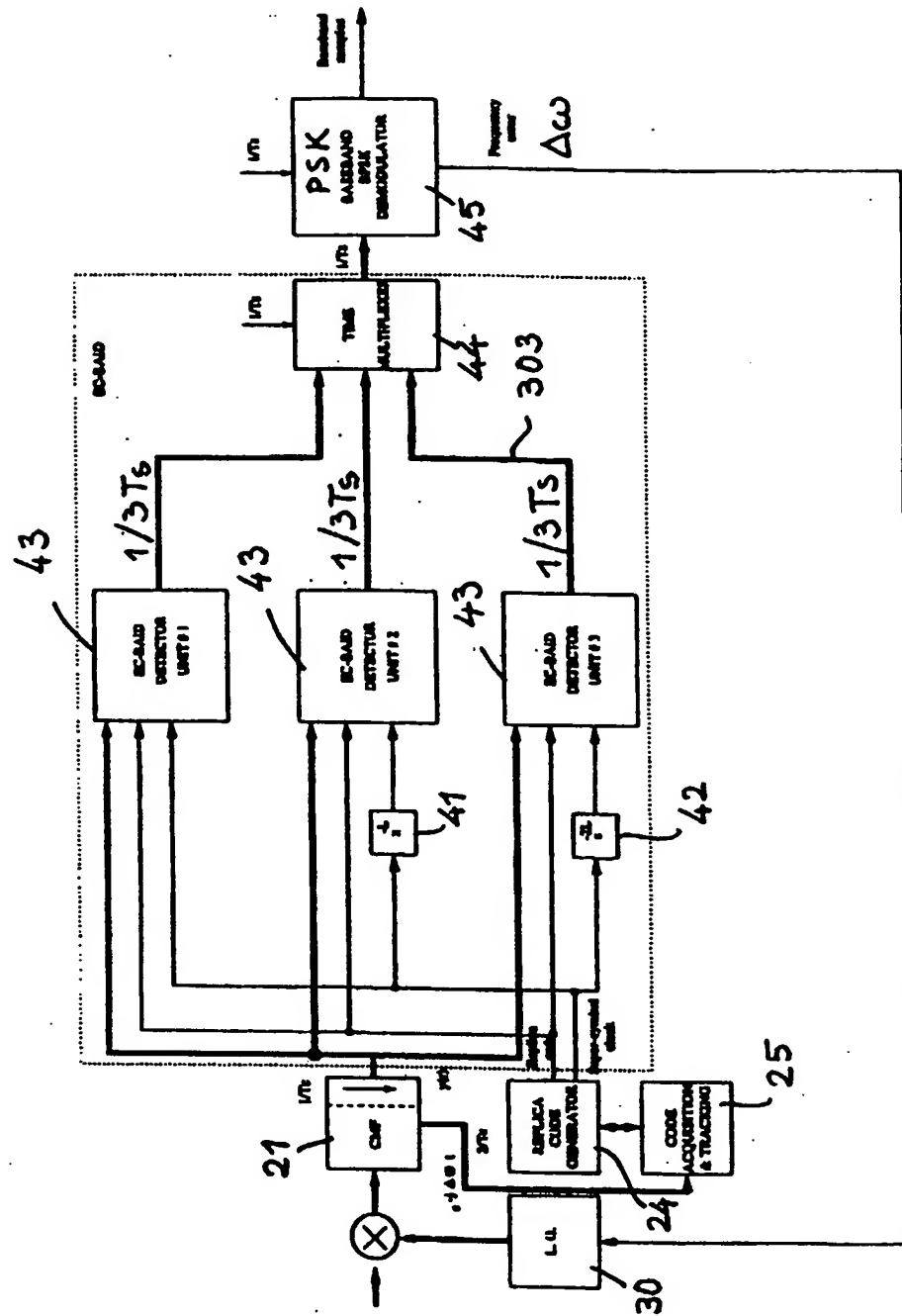
Top level block diagram for the receiver using the C-BAID with no oversampling $S = 1$ (dashed blocks only required for dual-BPSK signal format)

FIG. 2



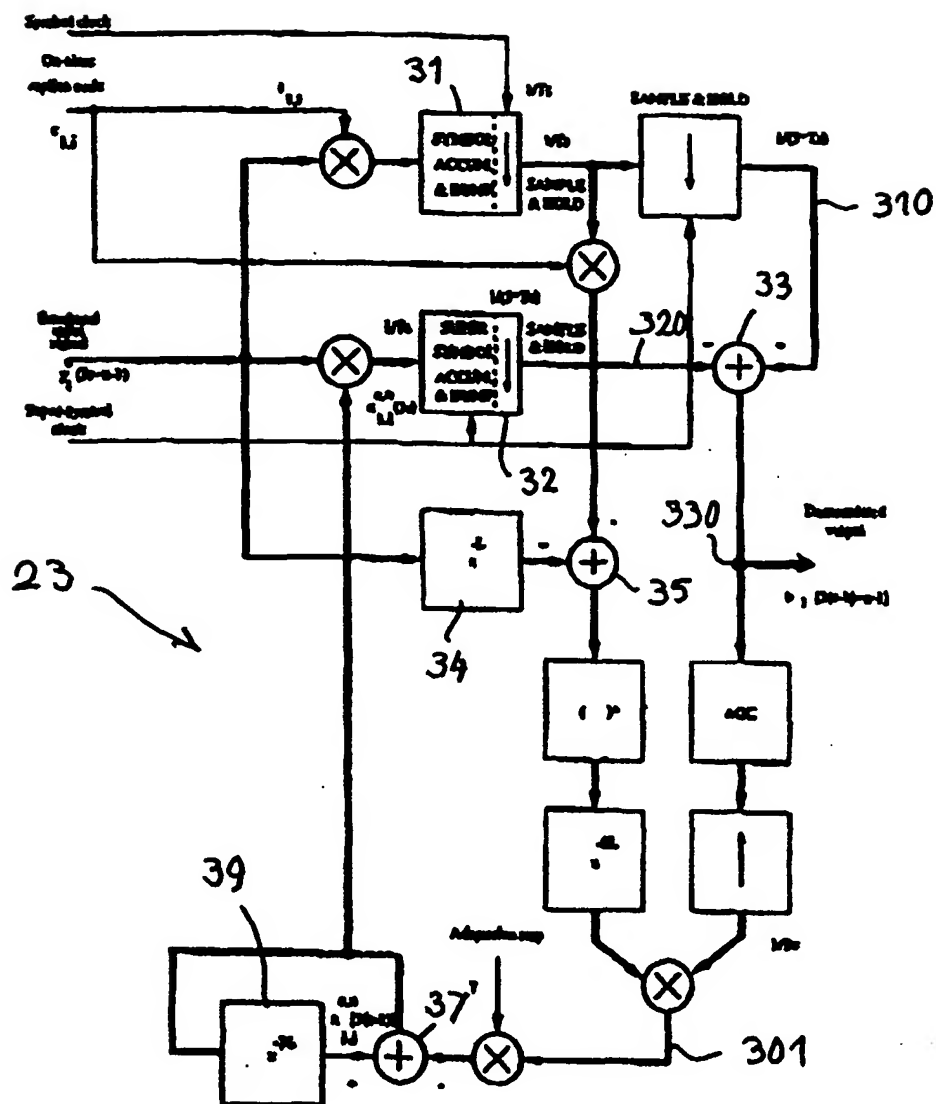
C-BAD functional block diagram

FIG. 3



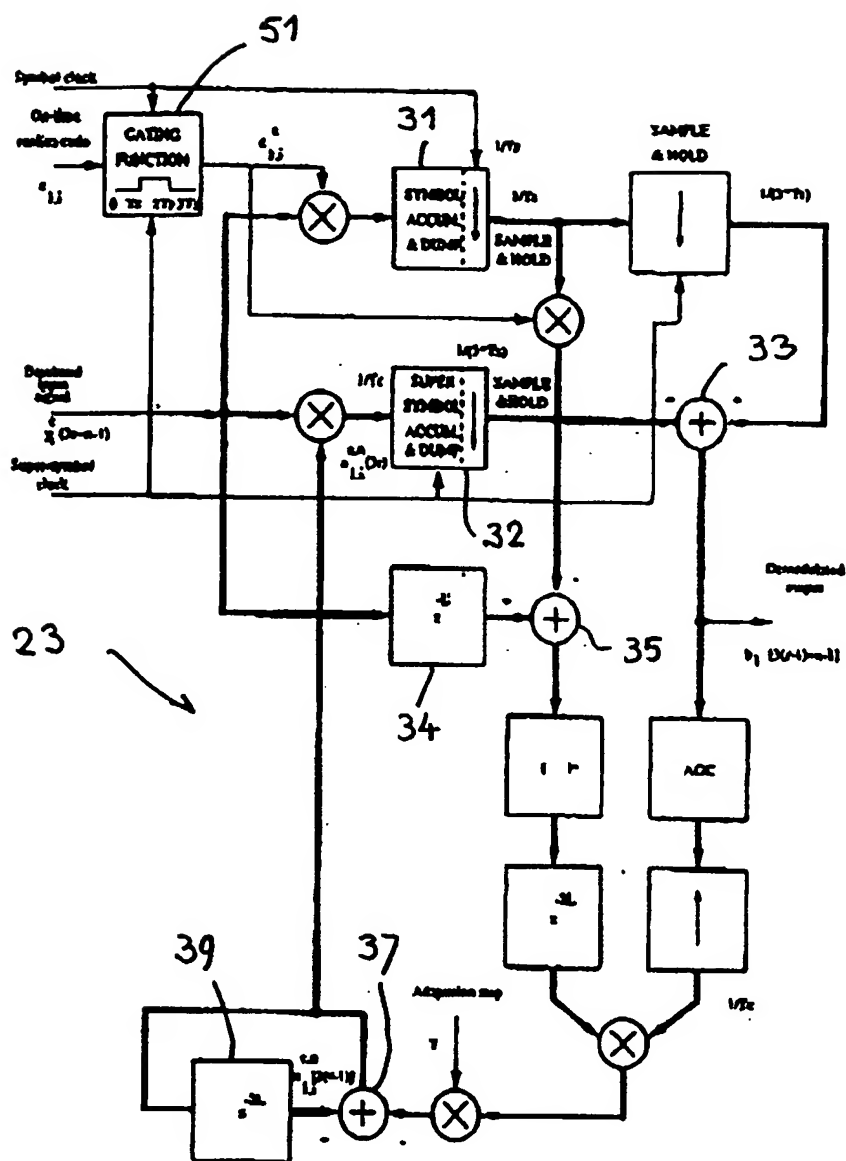
EC-BAID top level functional block diagram

FIG. 4



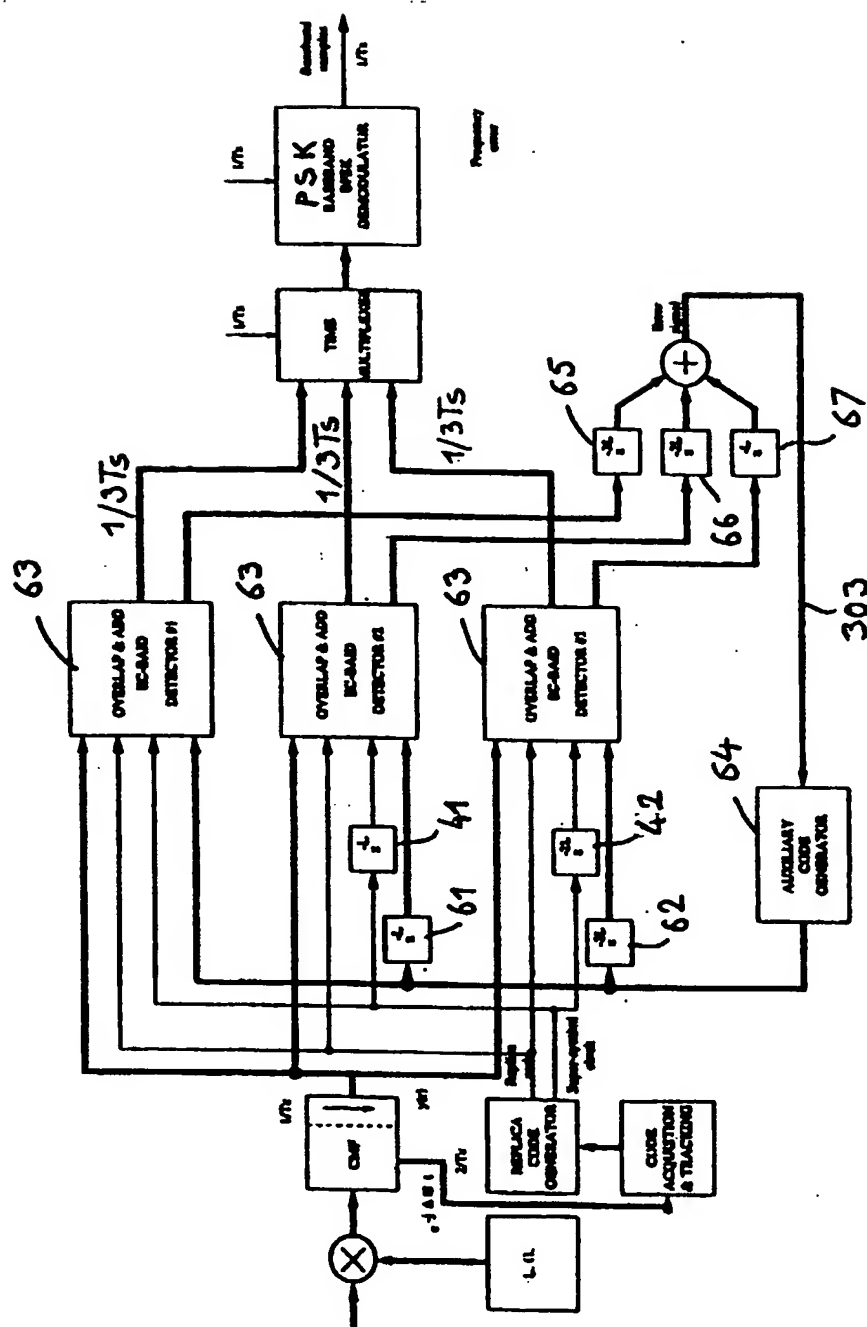
Baseline EC-BAID I detector unit functional block diagram

FIG. 5a



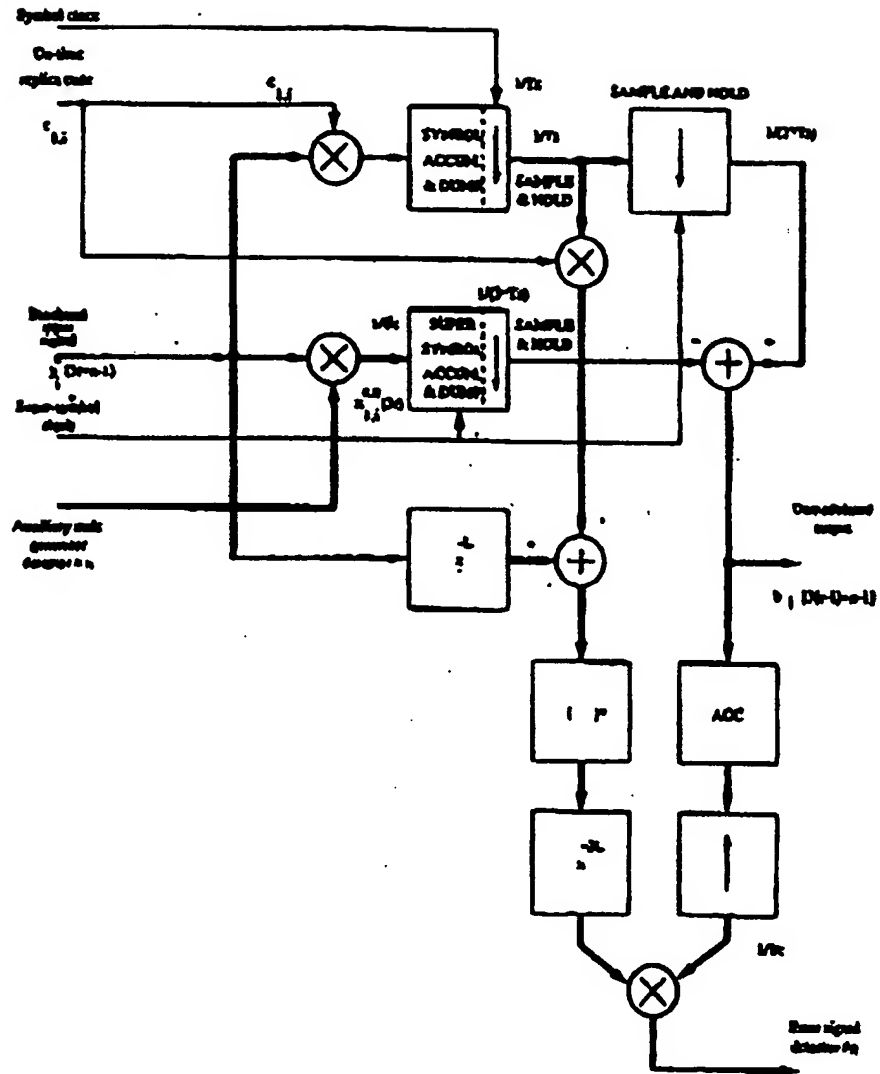
Baseline EC-BAID-II detector functional block diagram

FIG. 5b



Overlap and add EC-BAID top level functional block diagram

FIG. 6



Overlap and add EC-BAID I detector unit functional block diagram

FIG. 7a

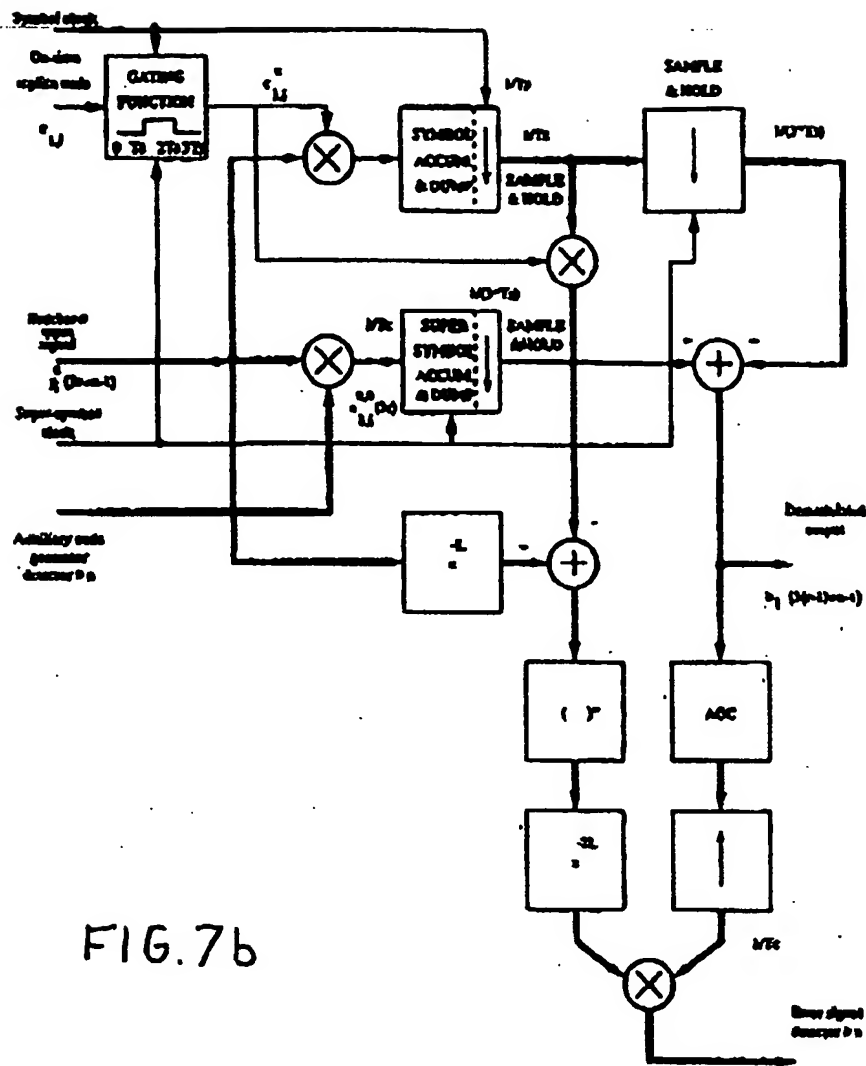


FIG. 7b

Overlap and add EC-BAID-II detector unit functional block diagram

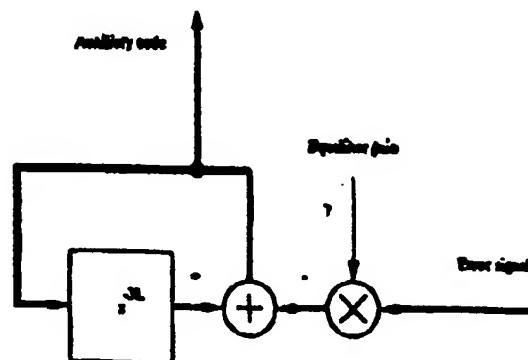
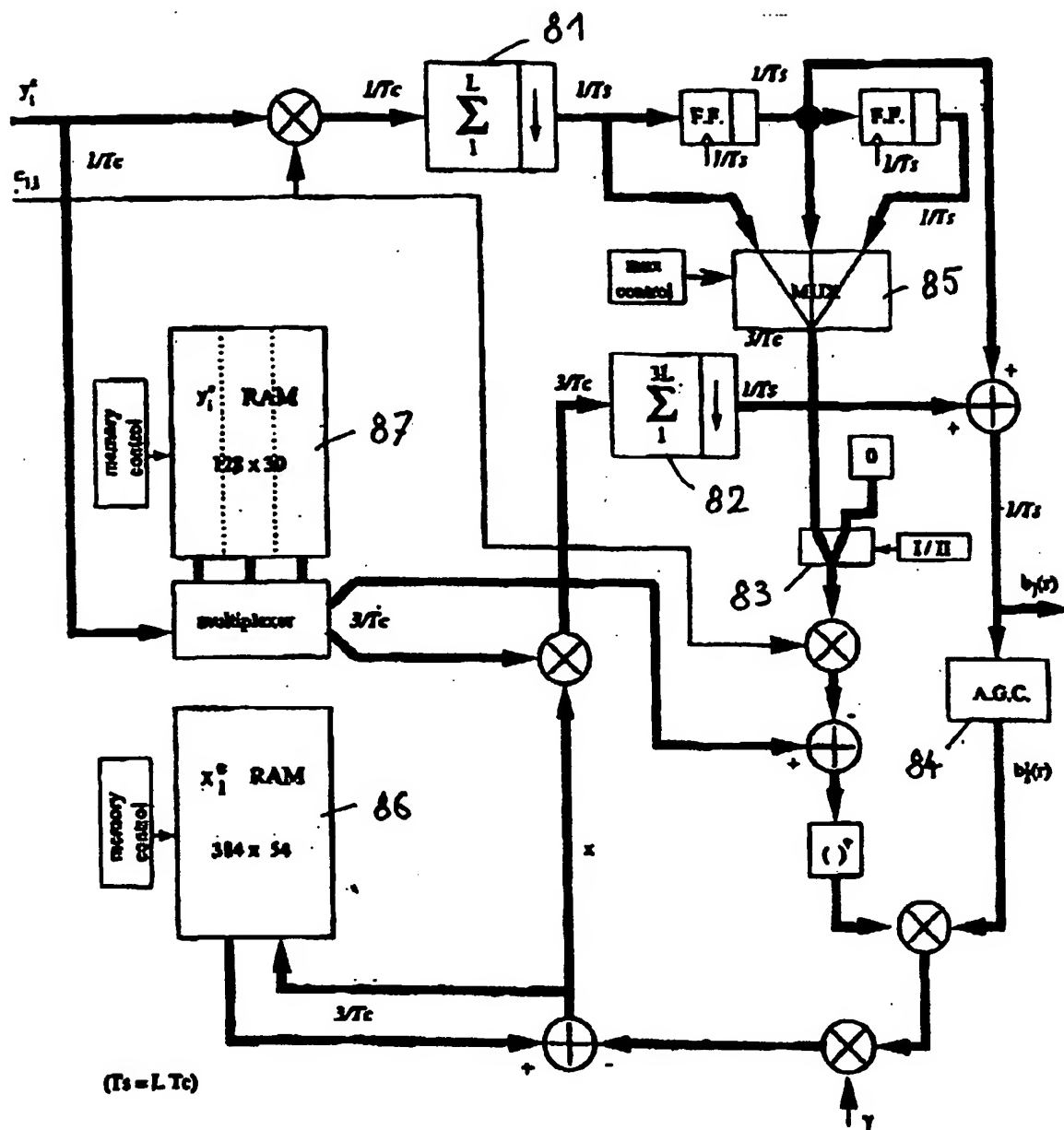


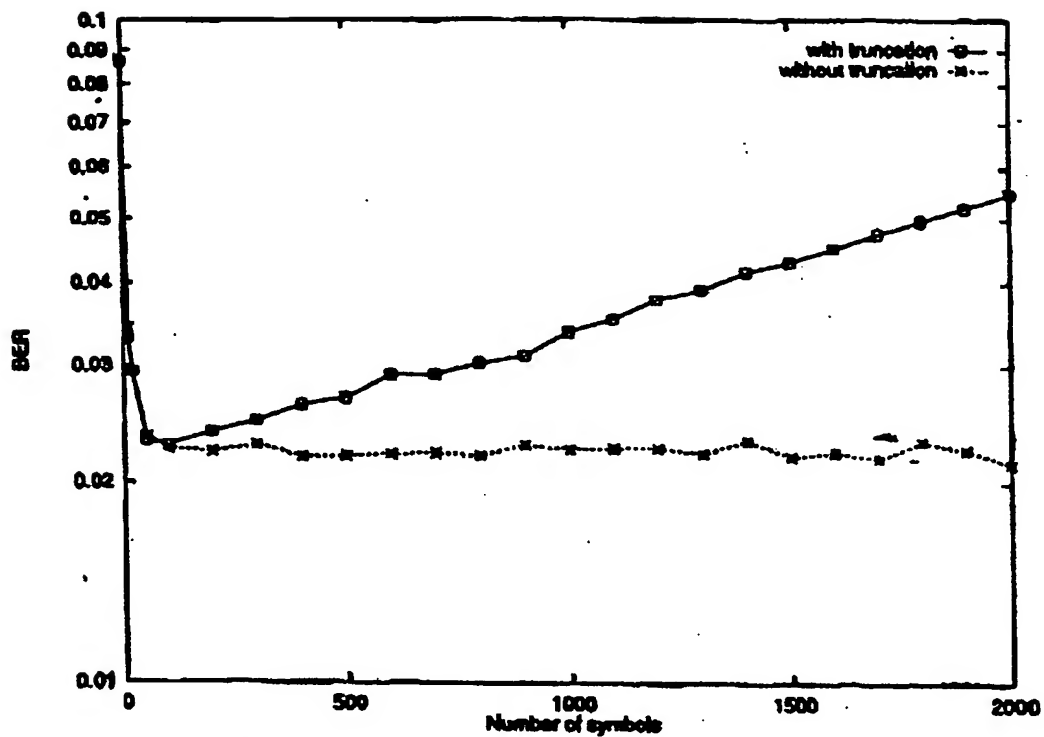
FIG. 7c

Overlap and add EC-BAID auxiliary code generator unit block diagram



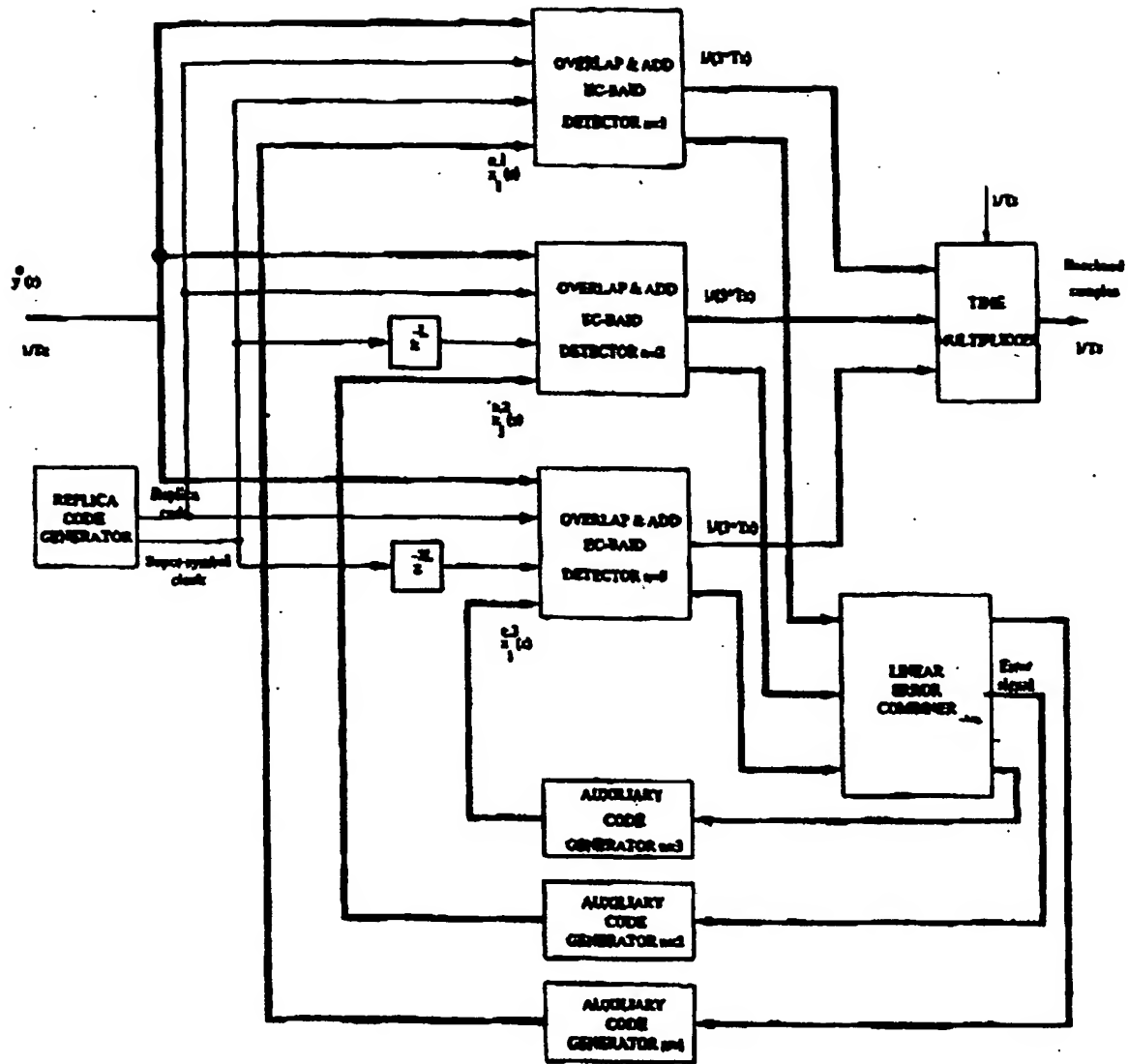
Optimised architecture of the EC-BAID "Select and Add".

FIG. 8



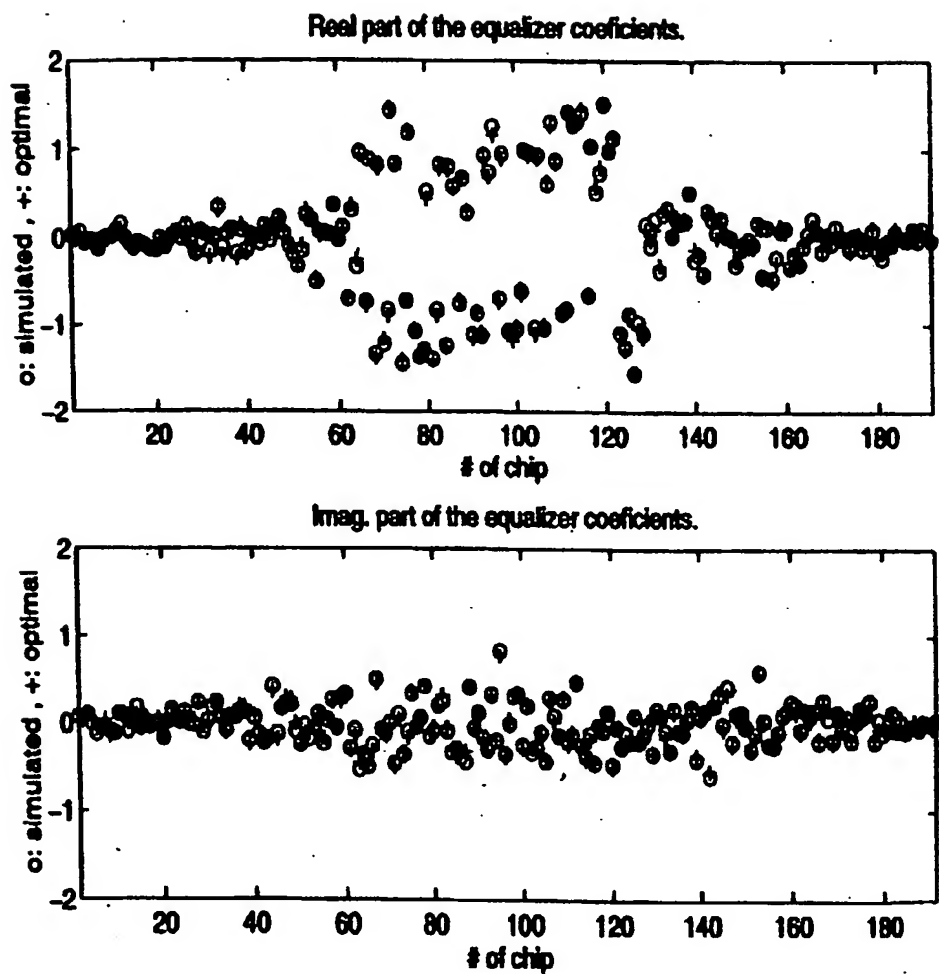
BER system performance with and without error signal truncation effects for $L = 128$, $N = 64$, $E_b/N_0 = 6$ dB and $C/I = -6$ dB.

FIG. 9



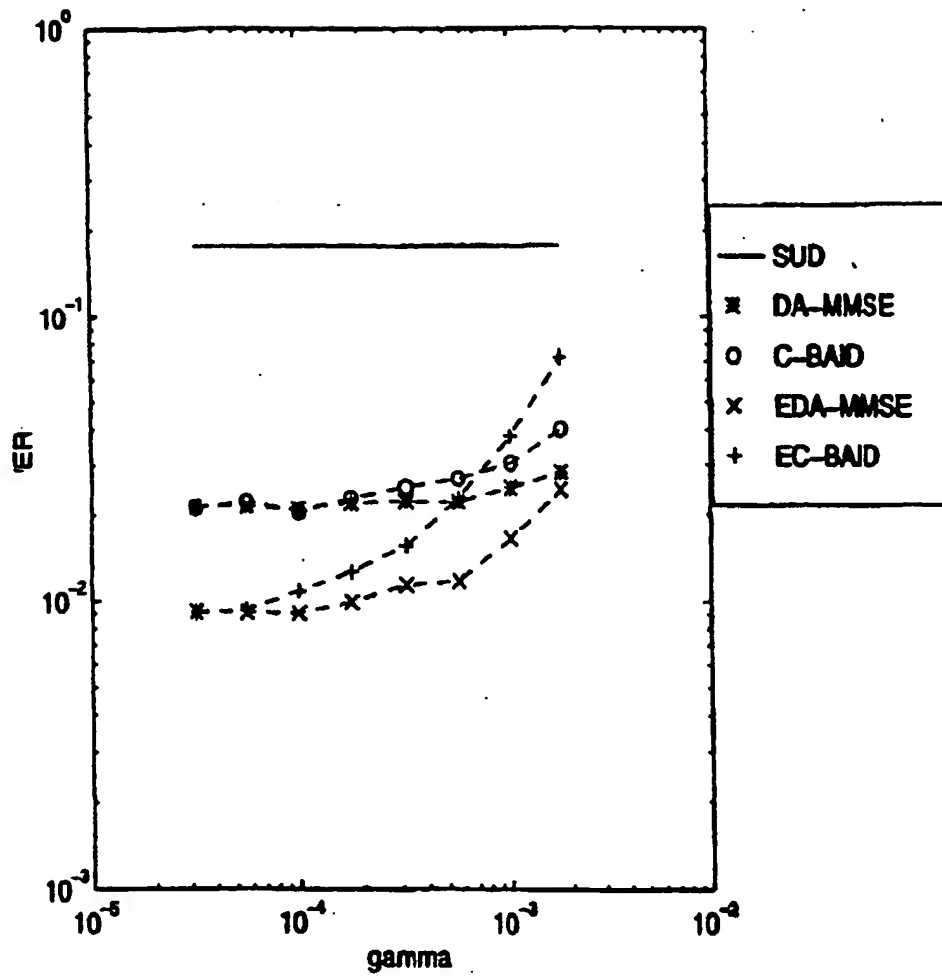
Linear Combiner EC-BAID top level functional block diagram.

FIG. 10



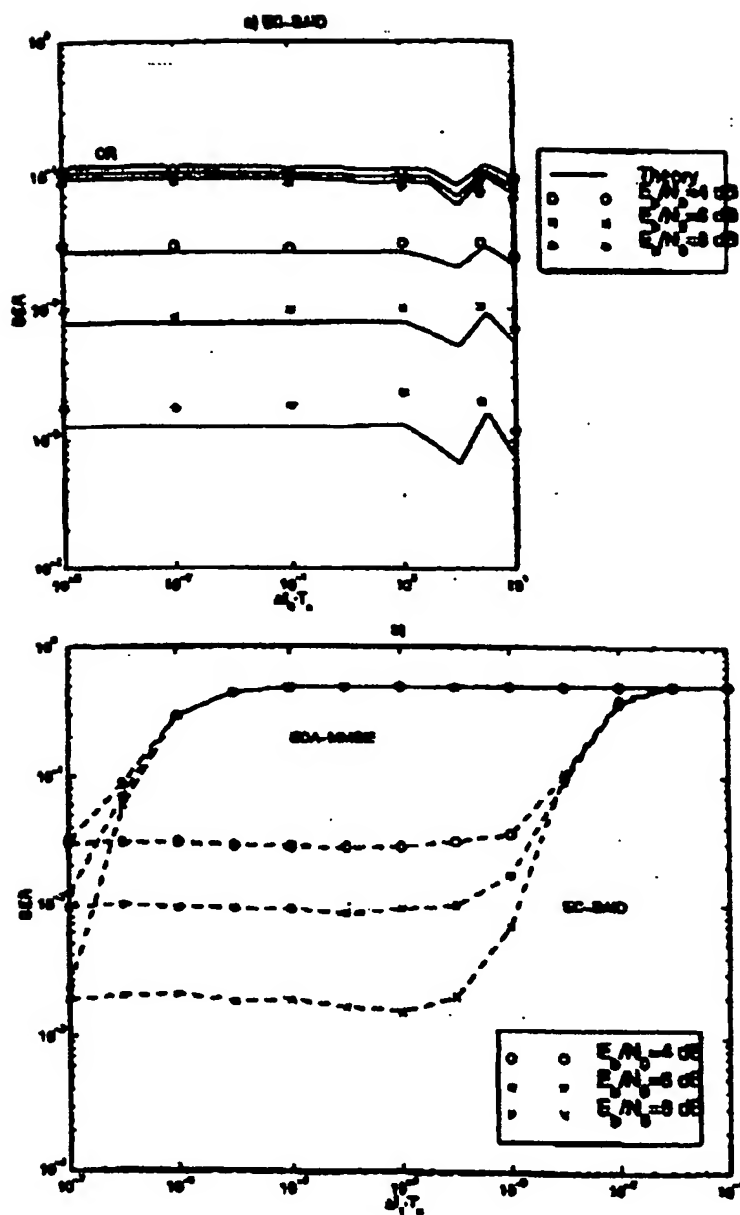
EC-BAID steady state detector coefficients: $E_s/N_0 = 4$ dB, $L = 64$, $K = 19$,
 $[C/I]_{\infty} = -6$ dB, $\Delta f_i \cdot T_s = 1$, $\gamma = 1.2 \cdot 10^{-4}$.

FIG. 11



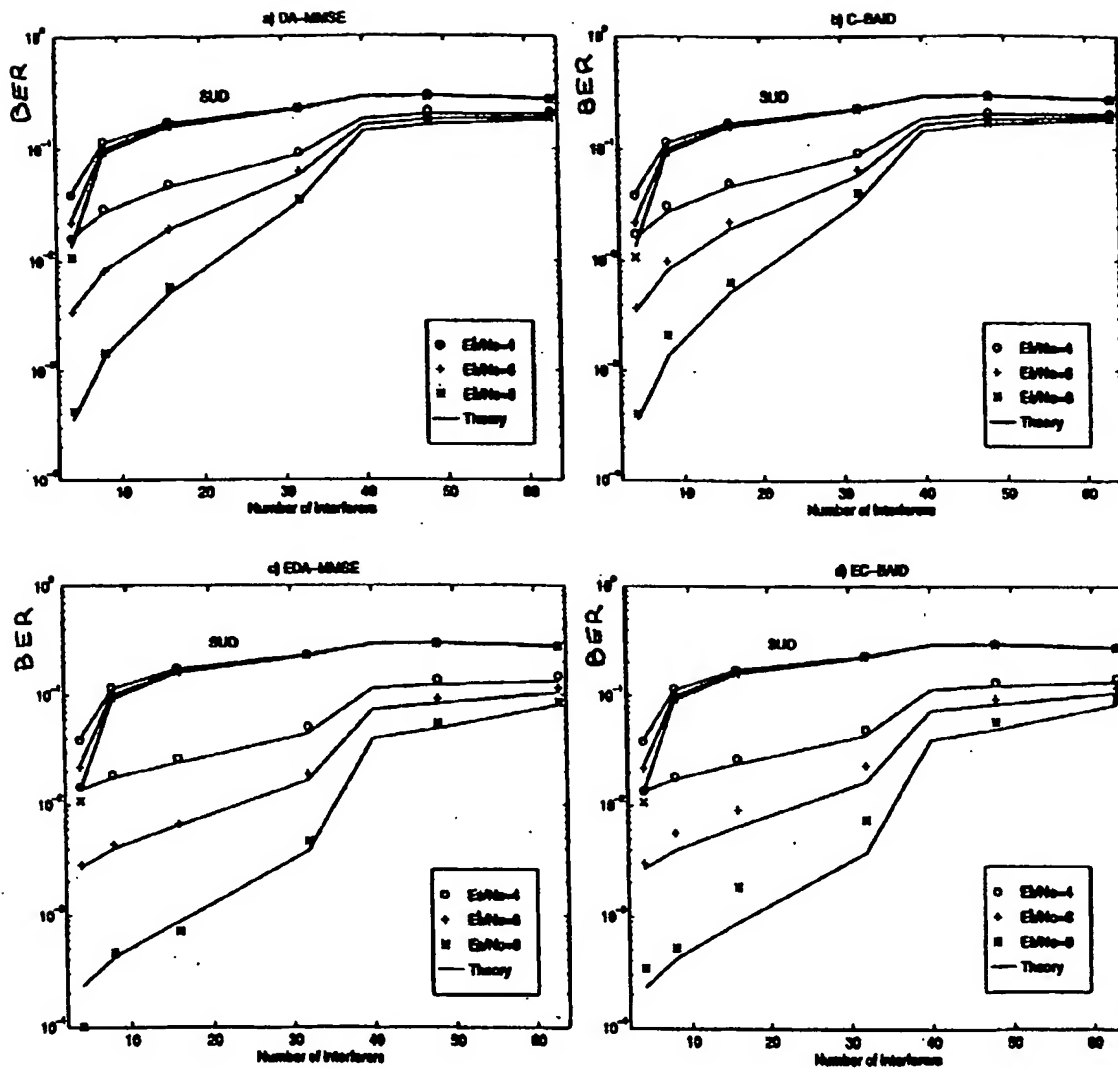
ER vs. γ for different detectors: $E_s/N_0 = 6$ dB, $L = 64$, $K = 19$, $[C/I]_{sc} = -6$

FIG. 12



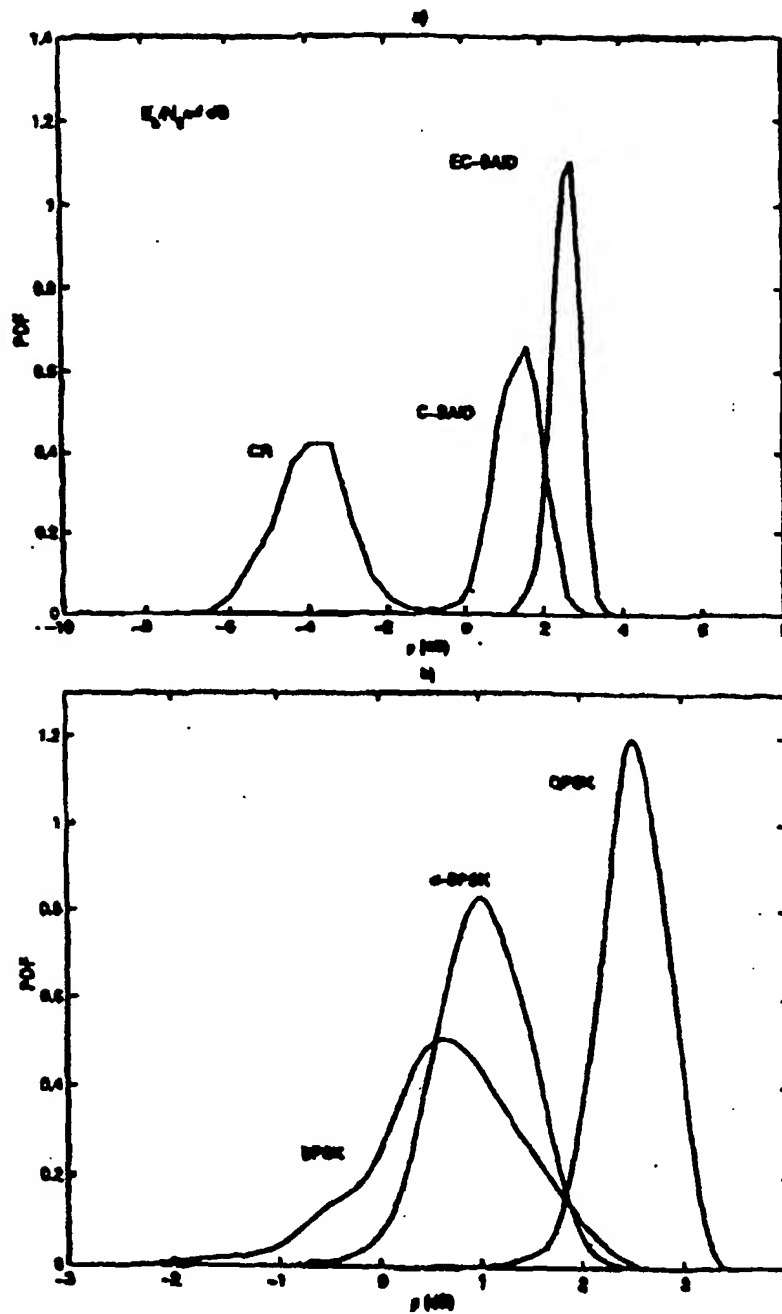
a) BER vs. $\Delta f_1 \cdot T$, for EC-BAID detector: $L = 64$, $K = 19$, $[C/I]_m = -6$ dB, b) BER vs. $\Delta f_1 \cdot T$, for different detectors: BPSK-RS, $L = 64$, $K = 19$, $[C/I]_m = -6$ dB.

FIG. 13



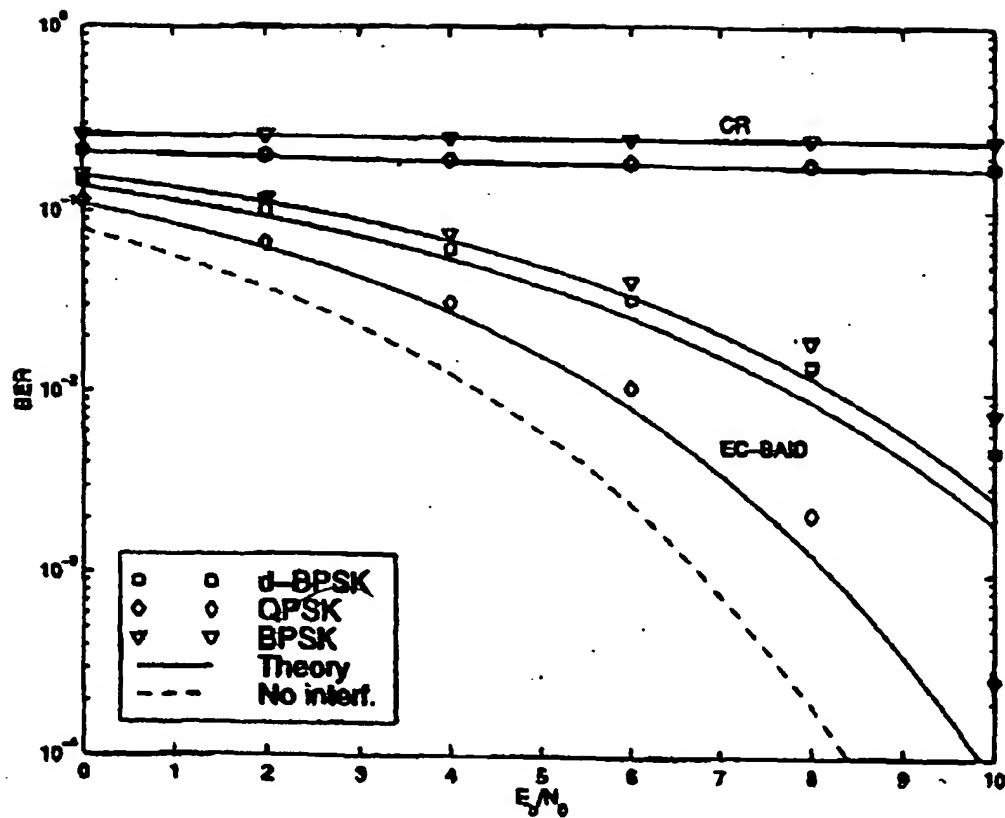
BER vs. Number of interferers for different detectors compared with SUD: $L = 64$, $[C/I]_{sc} = -6$ dB.

FIG. 14



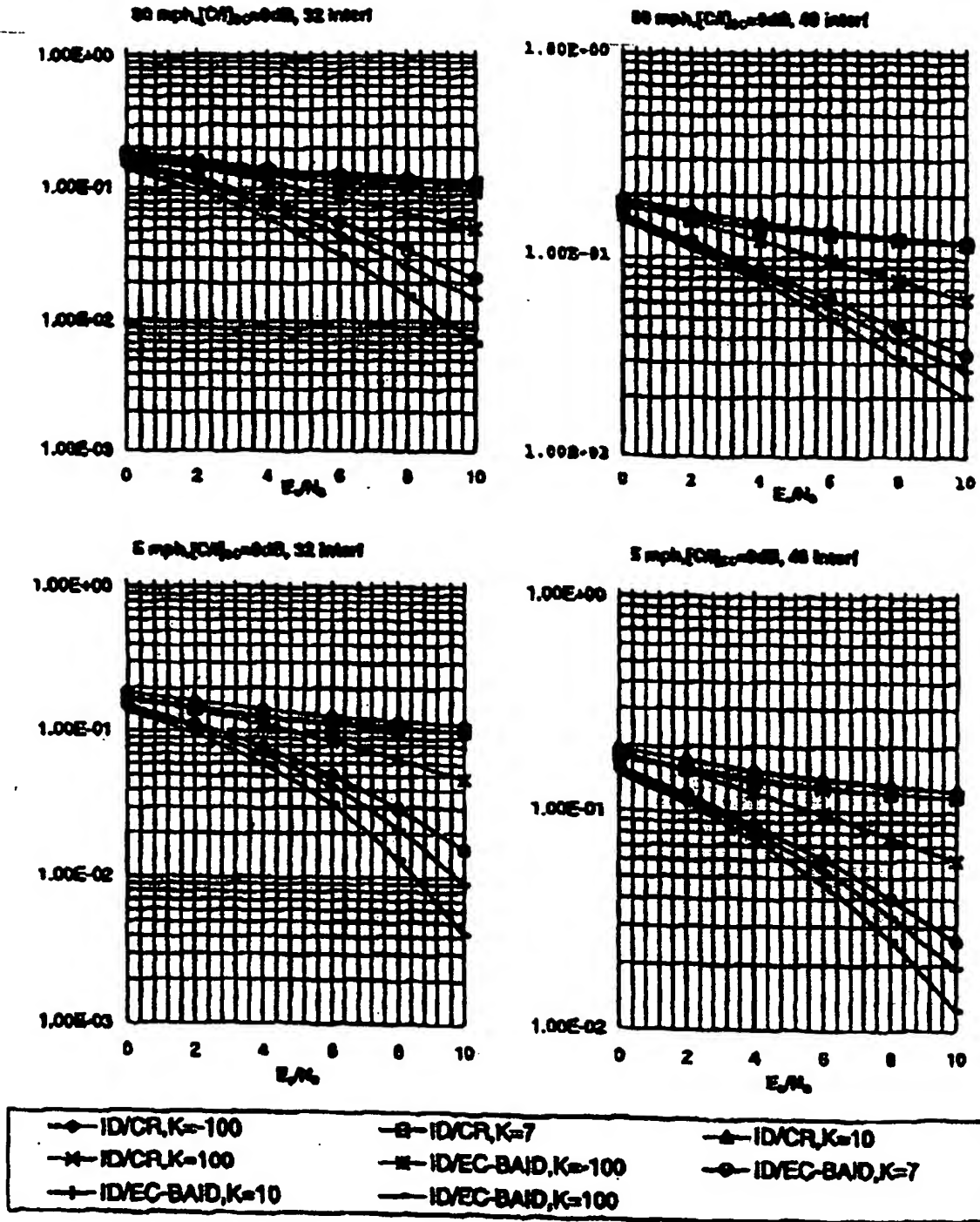
PDF of ρ for $E_b/N_0 = 4$ dB, $K = 19$, $[C/I]_{dc} = -6$ dB: a) BPSK-RS $L = 64$ CR, C-BAID, EC-BAID, b) EC-BAID $L = 32$ for BPSK-RS, $L = 64$ for d-BPSK and QPSK-RS

FIG. 15



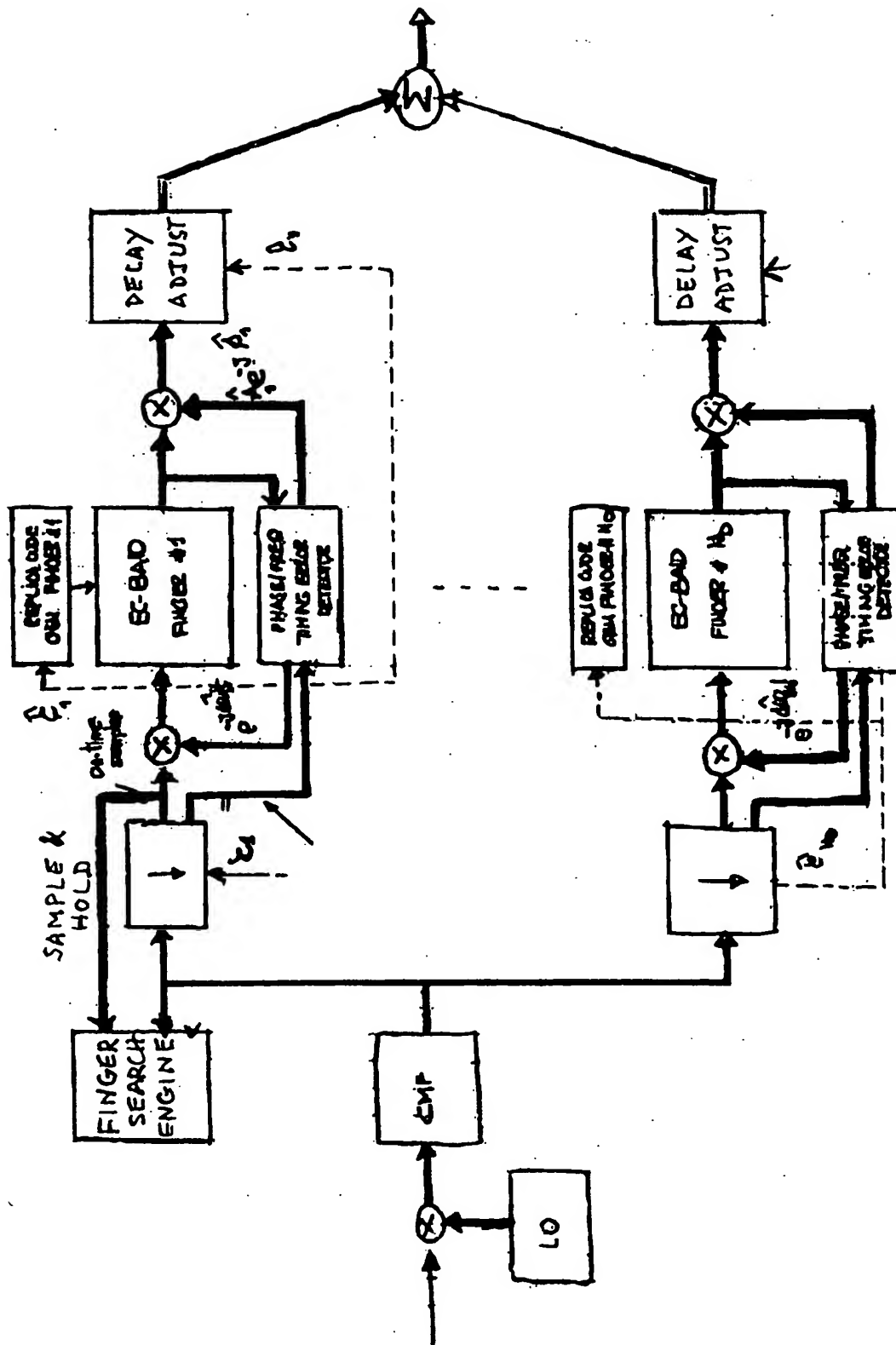
BER vs. E_b/N_0 for EC-BAID and CR with different signal formats: $L = 32$ for BPSK-RS, $L = 64$ for d-BPSK and QPSK-RS, $K = 19$, $[C/I]_{sc} = -6$ dB.

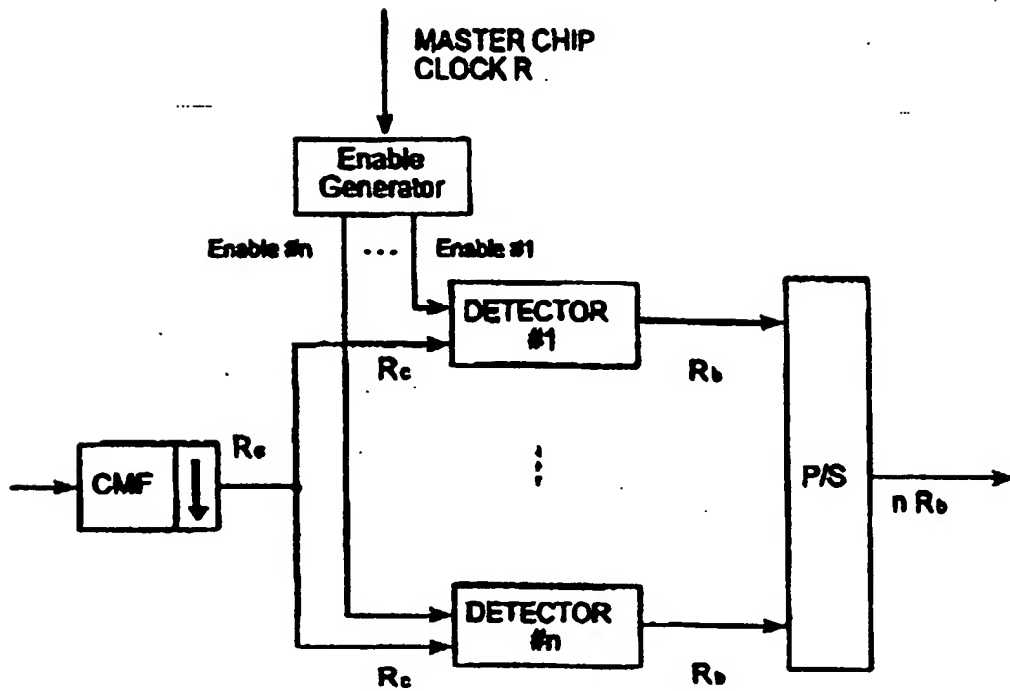
FIG.16



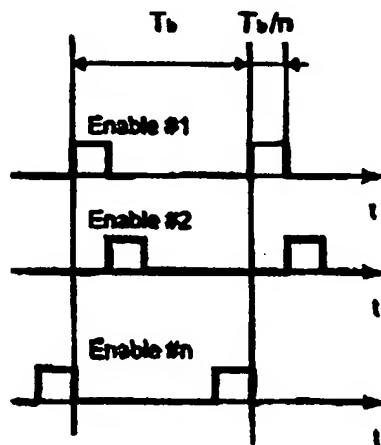
Simulation results for independent Ricean
channel for CR and EC-BAID detector. Ideal
channel estimation

FIG. 17

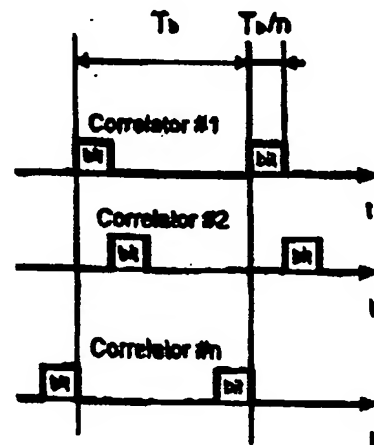




Enable Signals Time-Shifted by
 $T_b/n = T_c/L/n$



Correlators Output Symbols



Multi-rate Detector for the highest rate in the network

FIG. 19



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 99 87 0023

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	US 5 615 209 A (BOTTOMLEY GREGORY E) 25 March 1997 * abstract * * column 1, line 10 - line 23 * * column 2, line 50 - line 54 * * column 8, line 51 - column 9, line 51; claims 12-14 *	1-10	H04B1/707
A	EP 0 707 386 A (AT & T CORP) 17 April 1996 * abstract; figure 2 * * page 2, line 40 - line 50 * * page 5, line 34 - page 6, line 41 *	1-10	
A	HONIG M. L., MADOW U., VERDU S.: "Blind Adaptive Interference Suppression for Near-Far Resistant CDMA" 1994 IEEE GLOBECOM, vol. 1, 28 November 1994 - 2 December 1994, pages 379-384, XP002085278 San Francisco, USA * column 1, line 1 - line 12 * * column 2, line 24 - column 5, line 36 *	1-10	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			H04B
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 29 April 1999	Examiner Harris, E
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29-04-1999

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